

FIG. 1
PRIOR ART

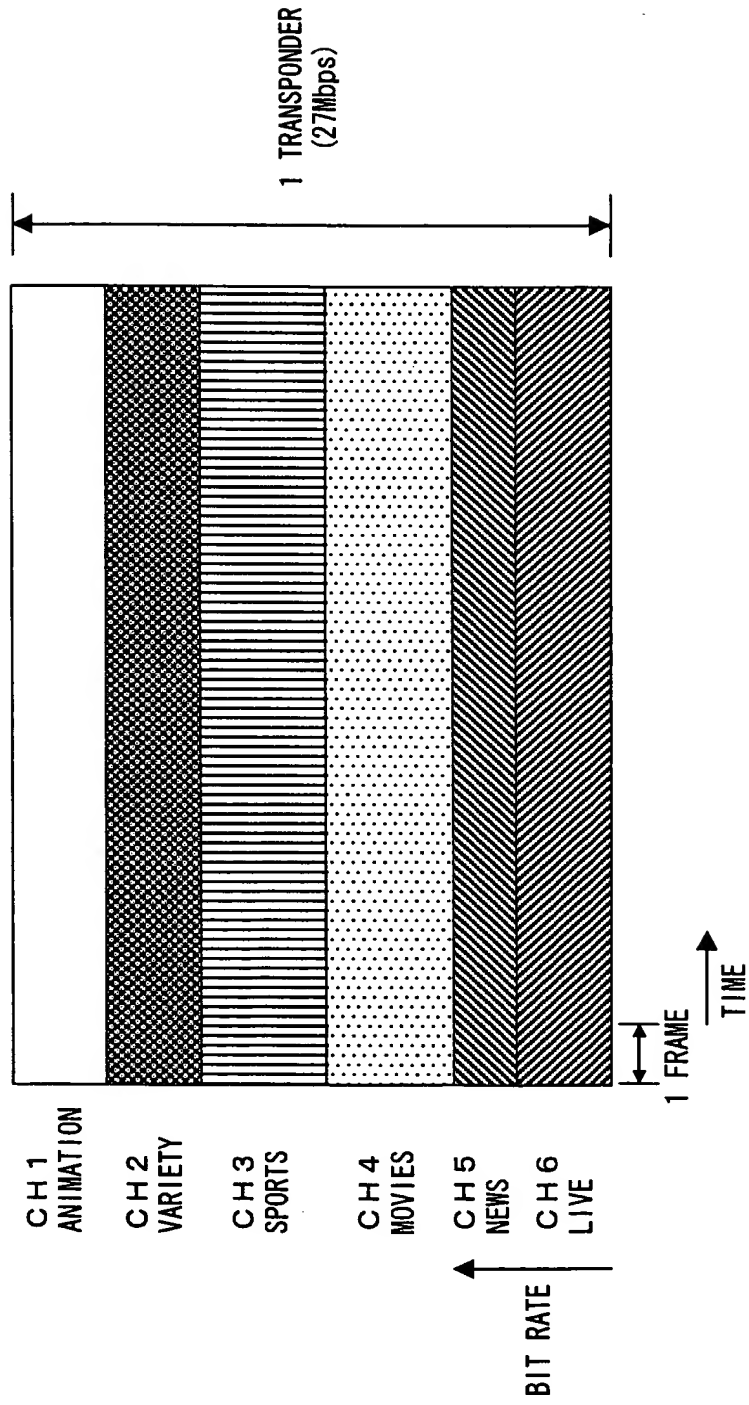


FIG. 2

TOT 20° 50310660

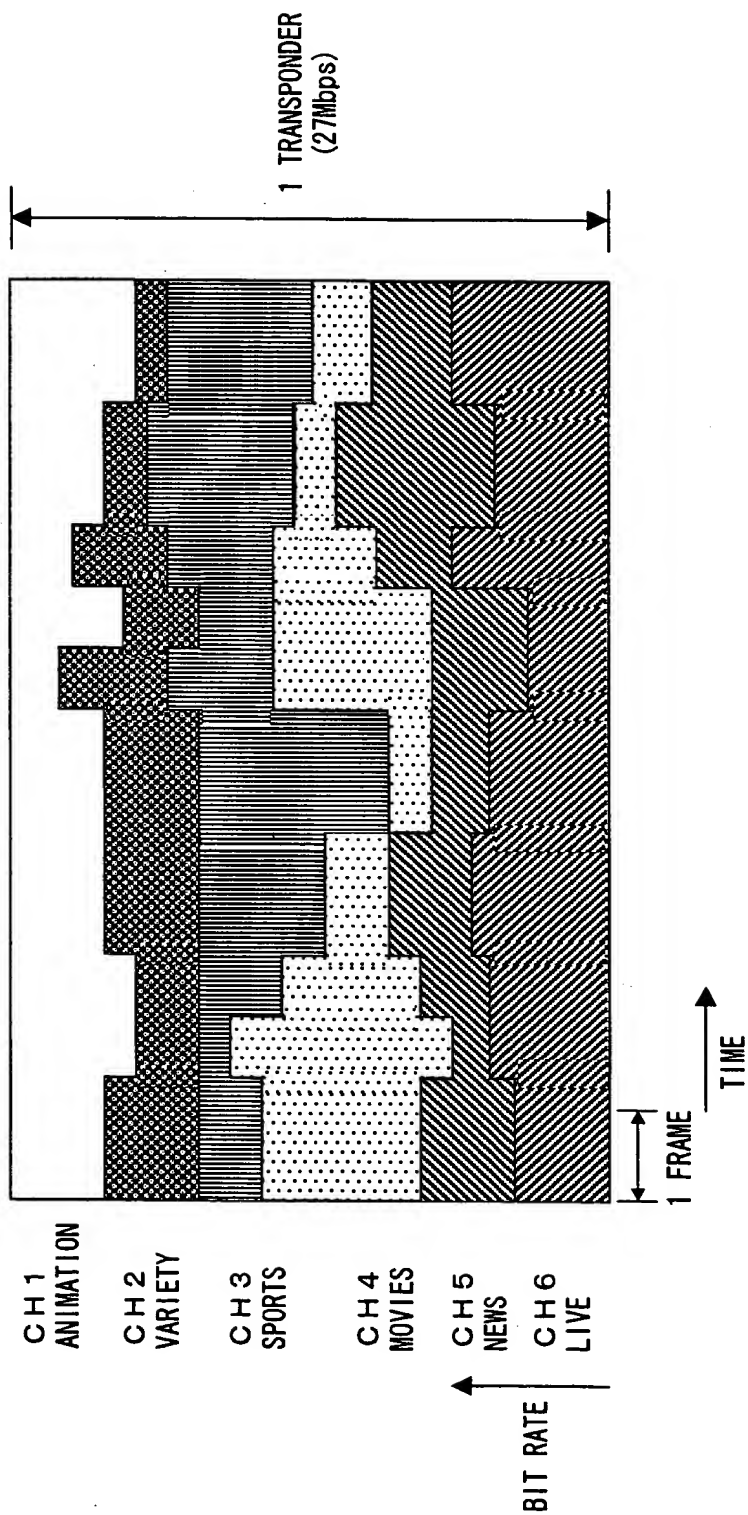


FIG. 3

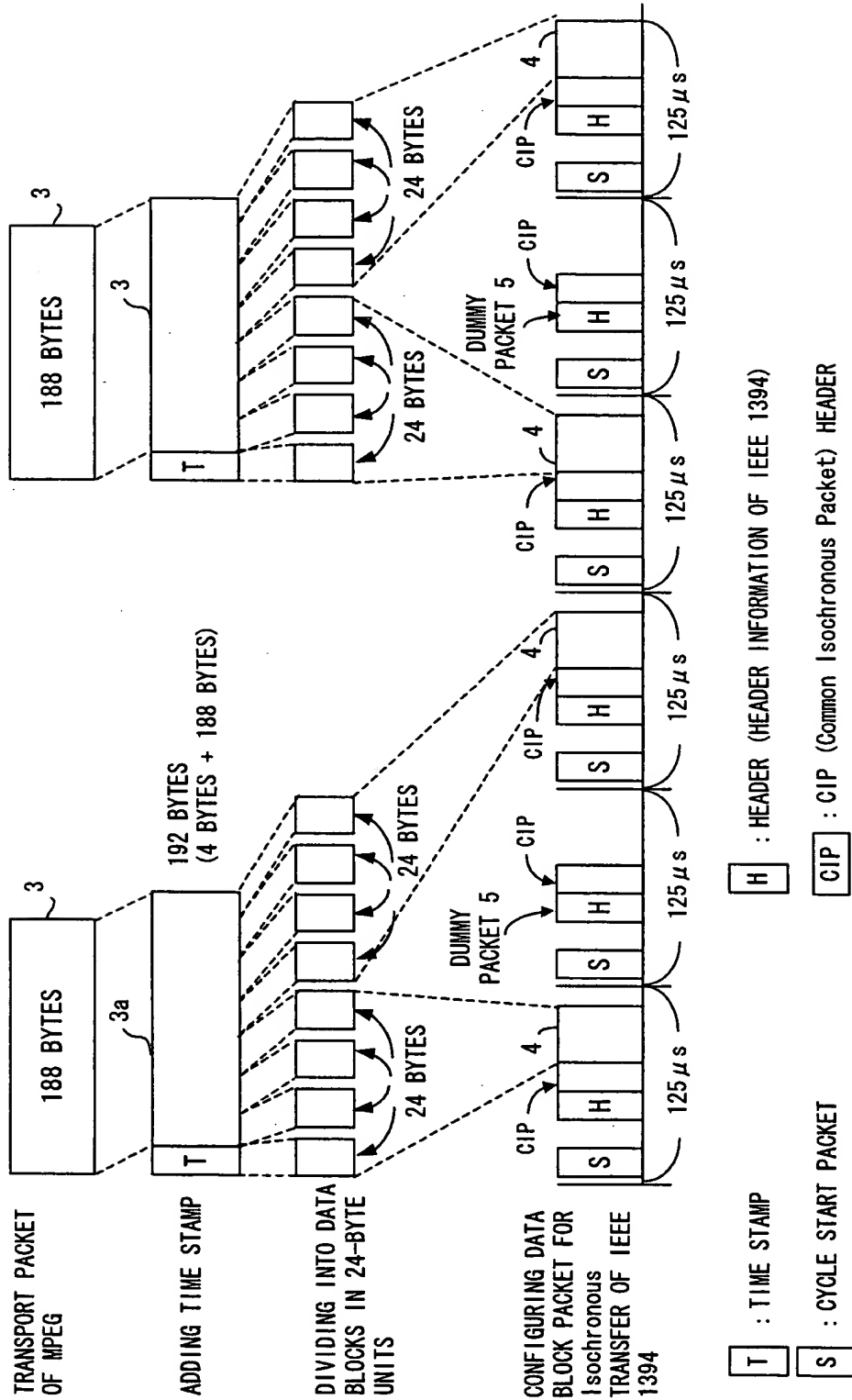


FIG. 4

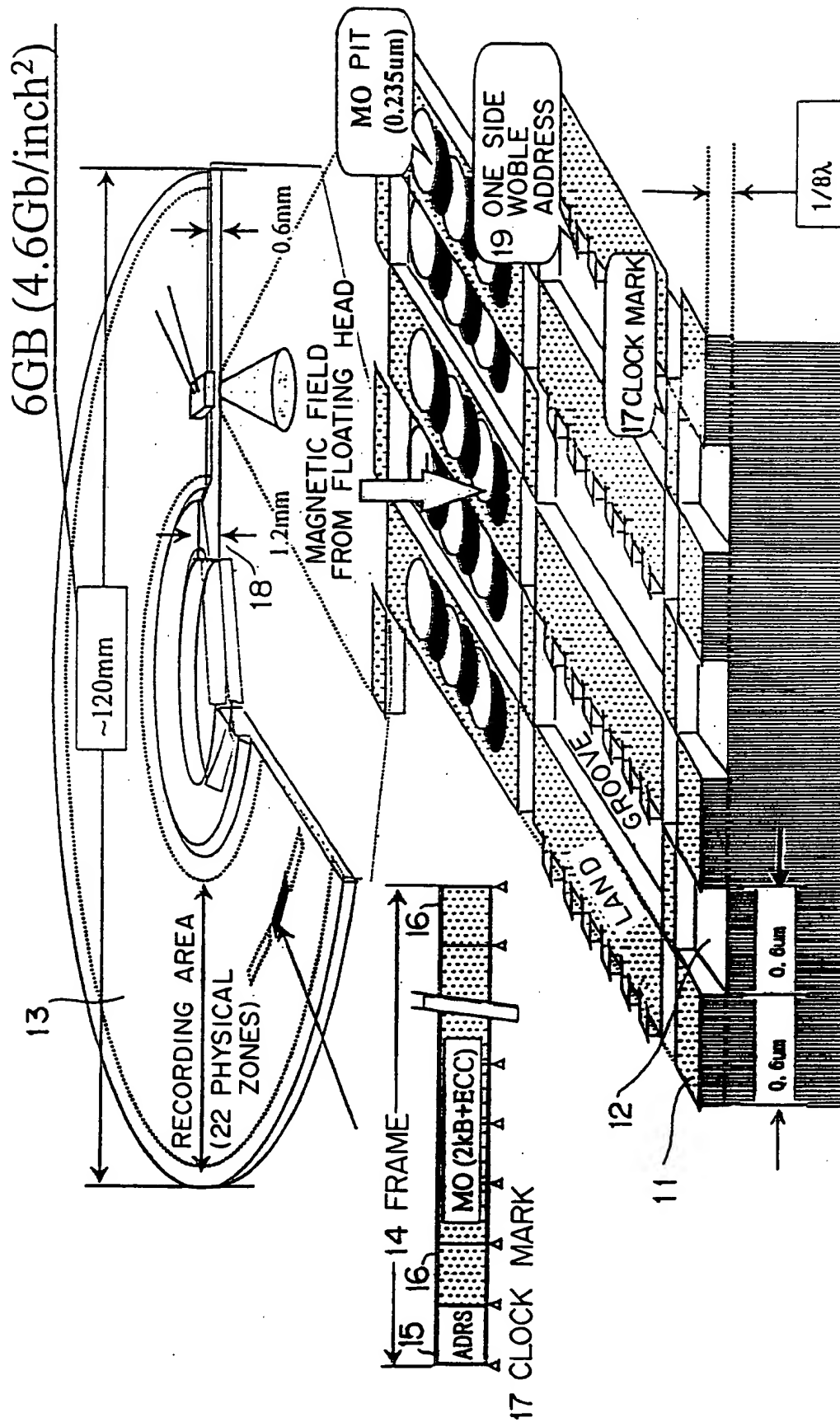


FIG. 5

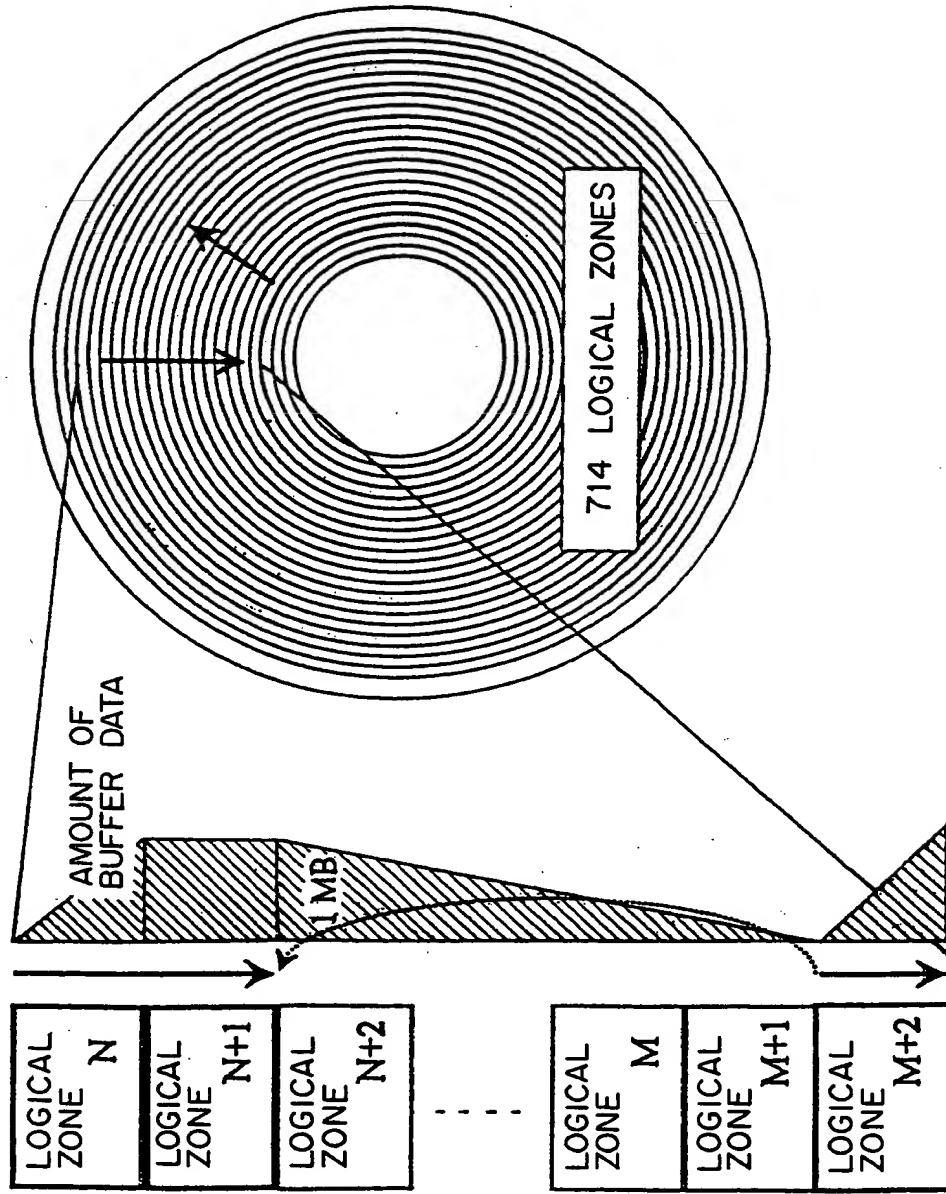


FIG. 6

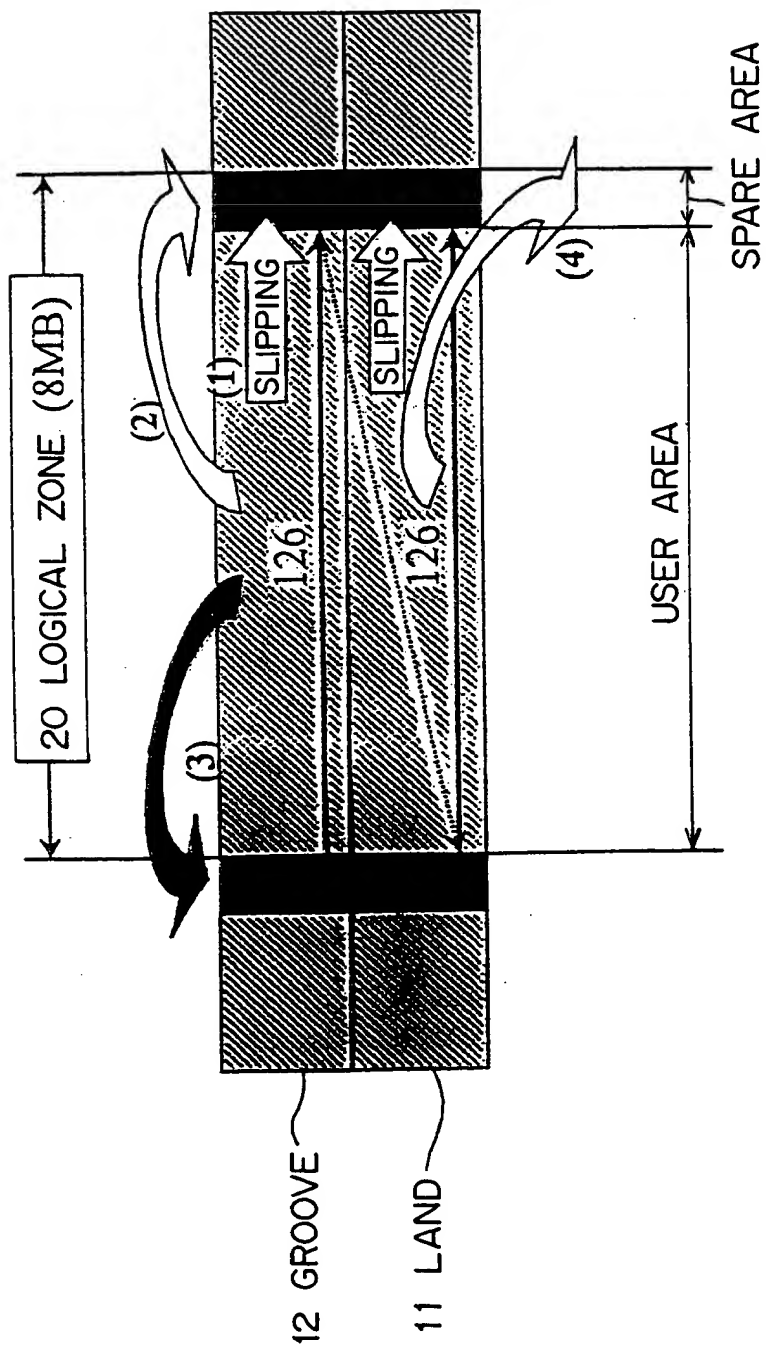


FIG. 7

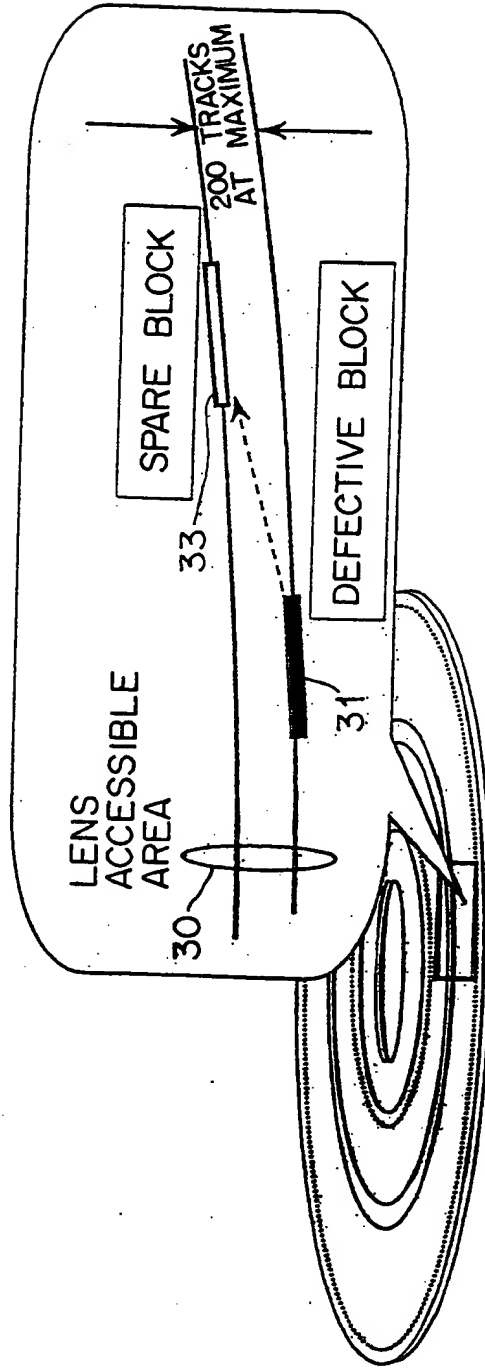


FIG. 8

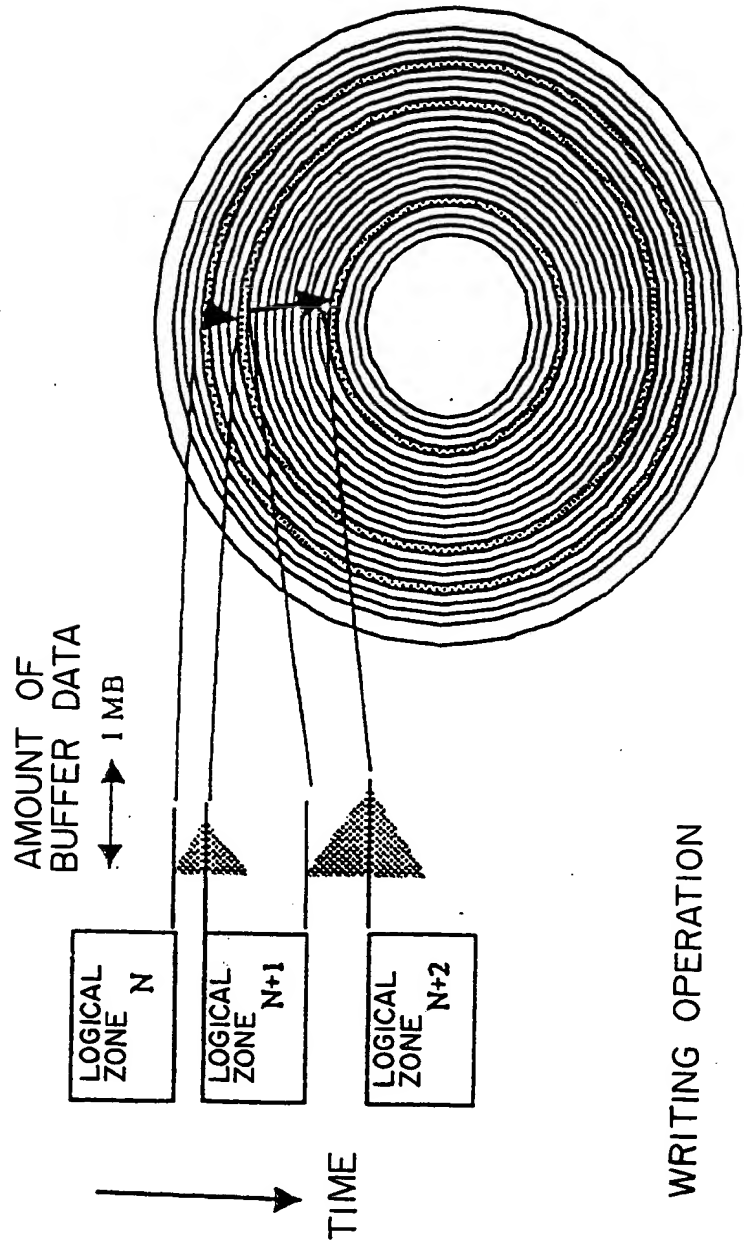
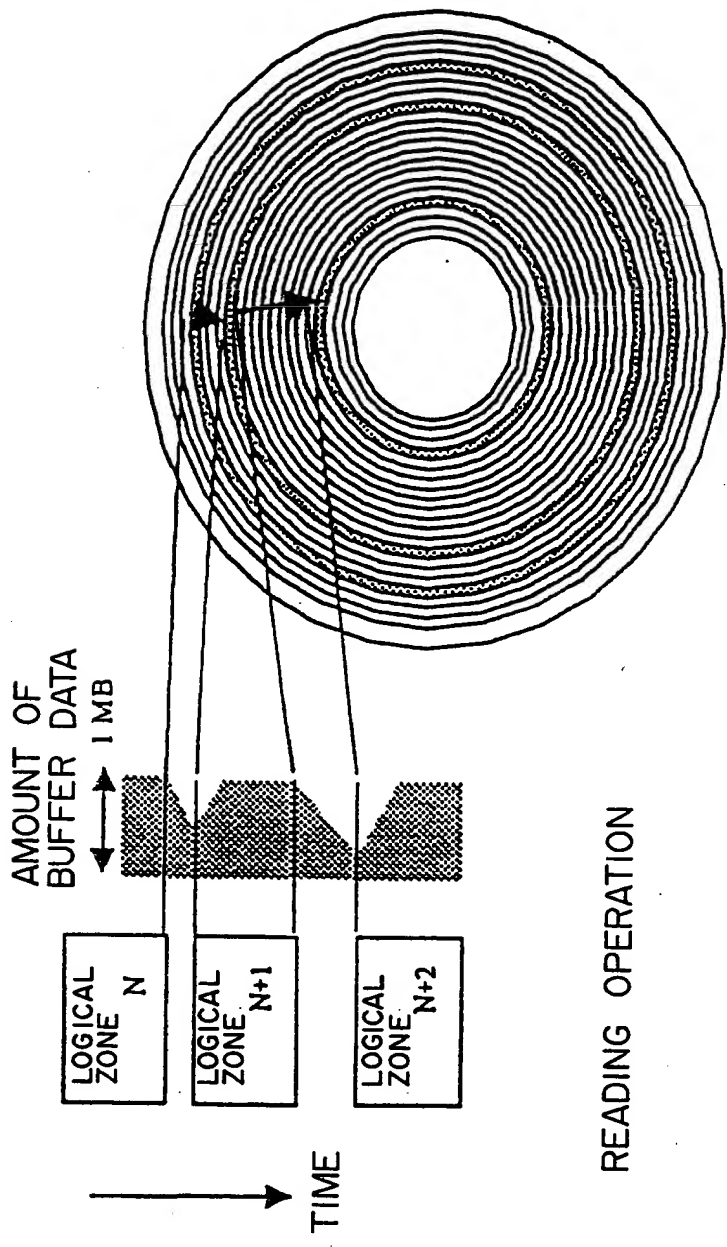


FIG. 9

TOP SECRET 50510660



READING OPERATION

FIG. 10

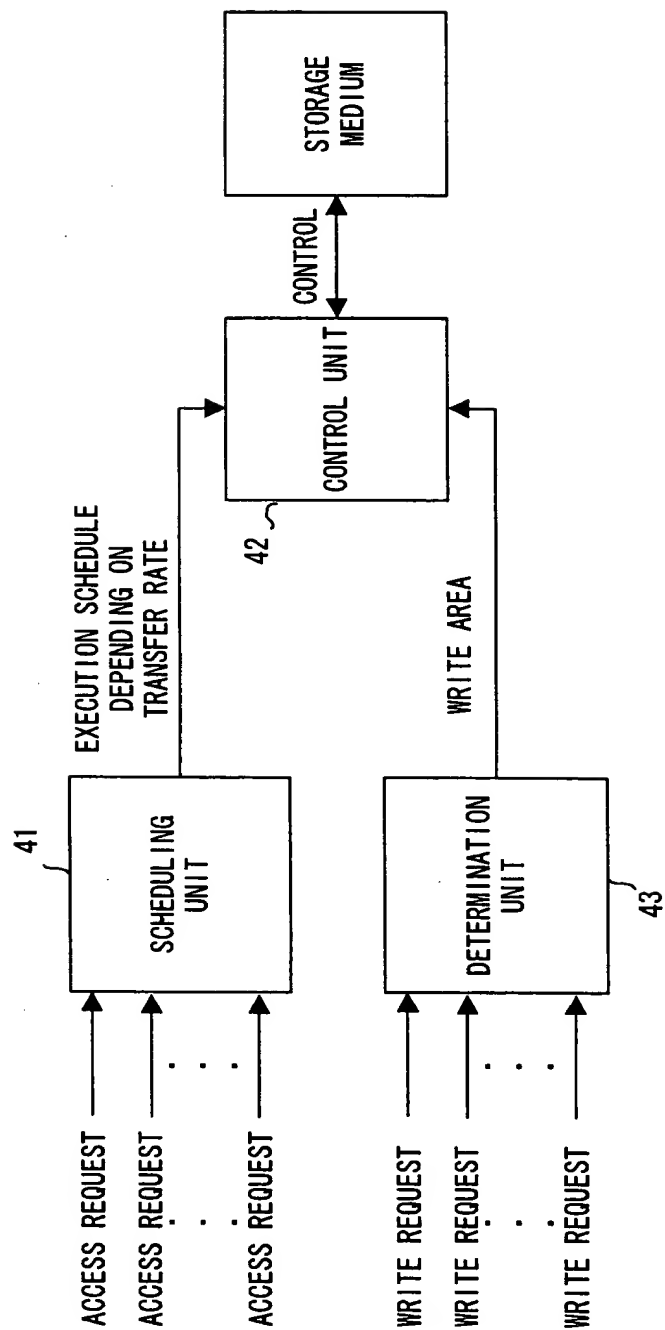


FIG. 11

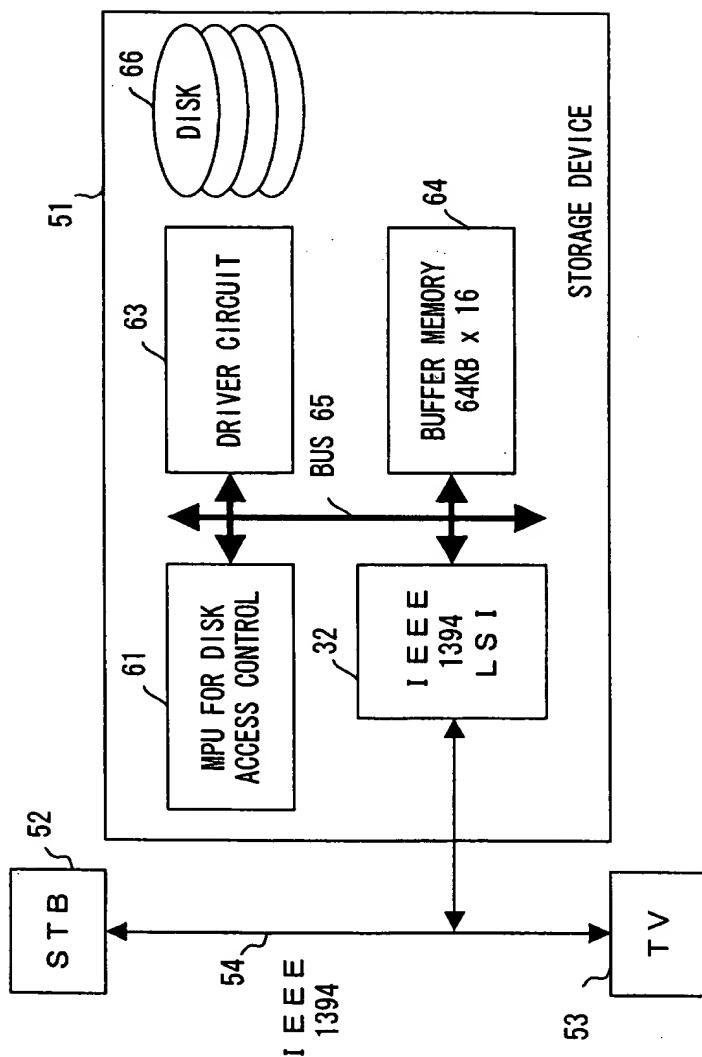
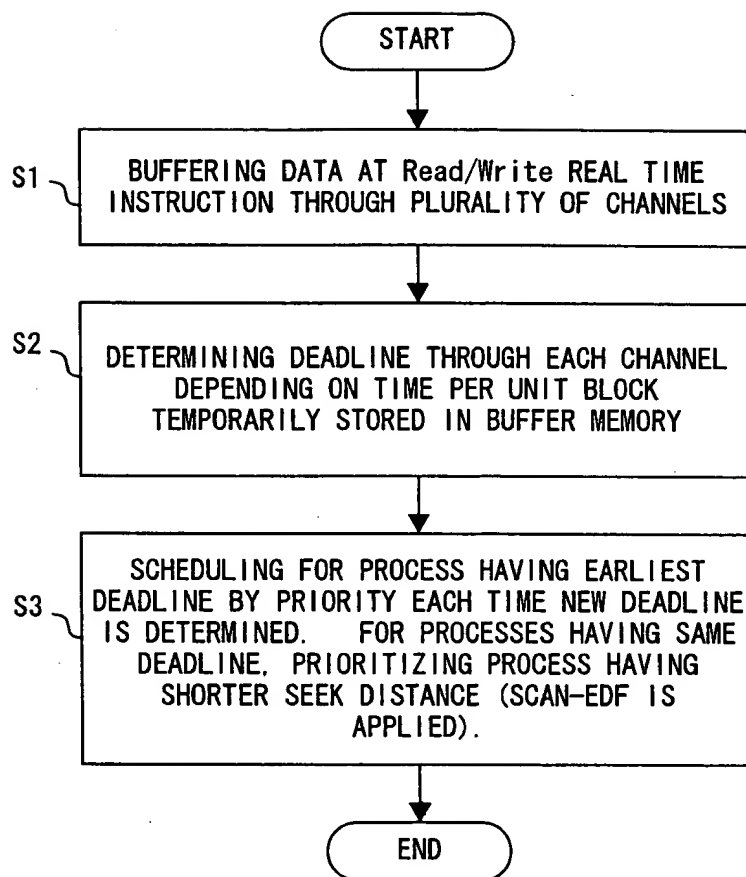


FIG. 12



F I G. 1 3

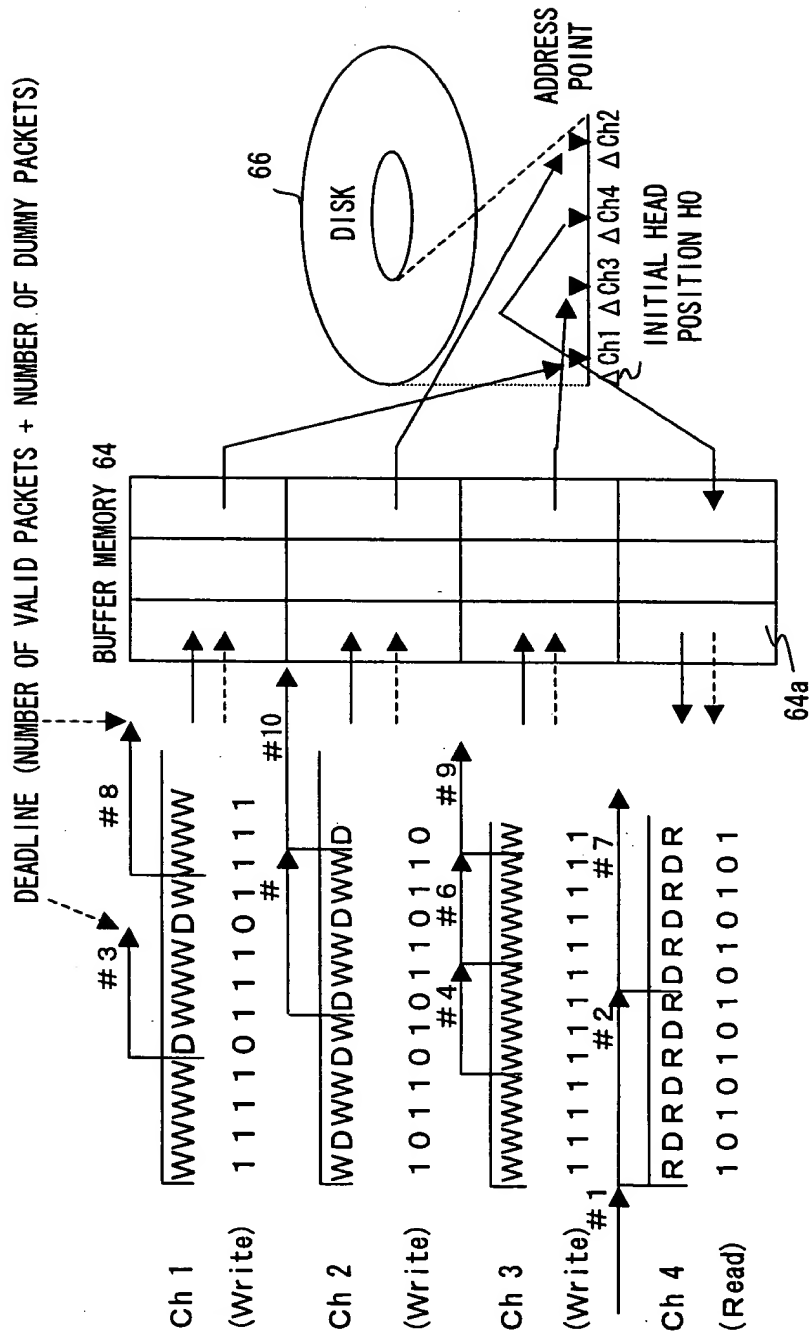


FIG. 14

TOP SECRET

MAXIMUM TRANSFER RATE (NUMBER OF BYTES/ NUMBER OF PACKETS)	DEADLINE INFORMATION	BINARY DATA	VALID DATA
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FIG. 15

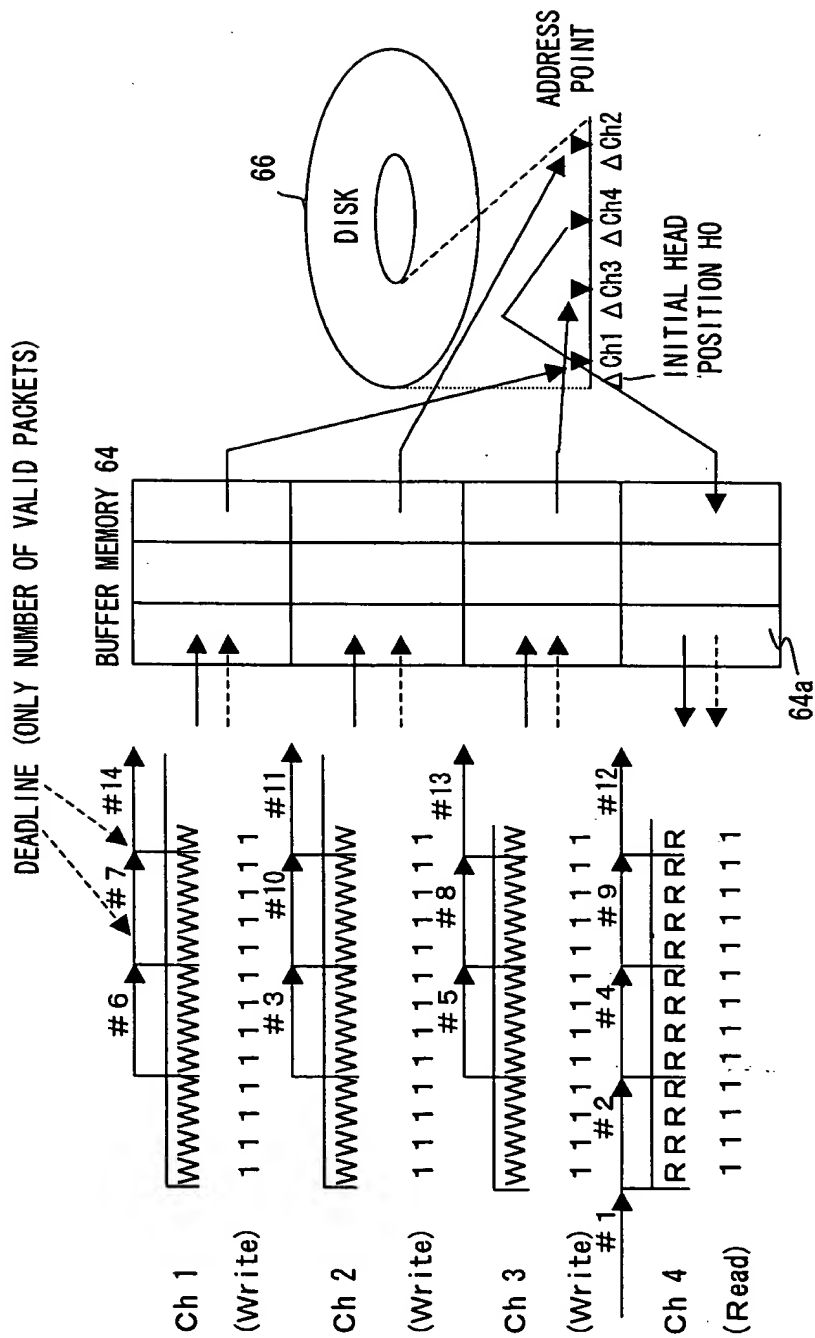


FIG. 16

70
S

PROCESS ORDER	DEADLINE T	R/W	CHANNEL C	BLOCK ADDRESS A ON DISK
1	T_i	W_i	C_i	A_i
\vdots	\vdots	\vdots	\vdots	\vdots
$m-1$	T_j	R_j	C_j	A_j
REGISTRATION → m	T_k	R_k	C_k	A_k
\vdots	\vdots	\vdots	\vdots	\vdots
$2N$				

Order (1) = {T, R/W, C, A}

FIG. 17

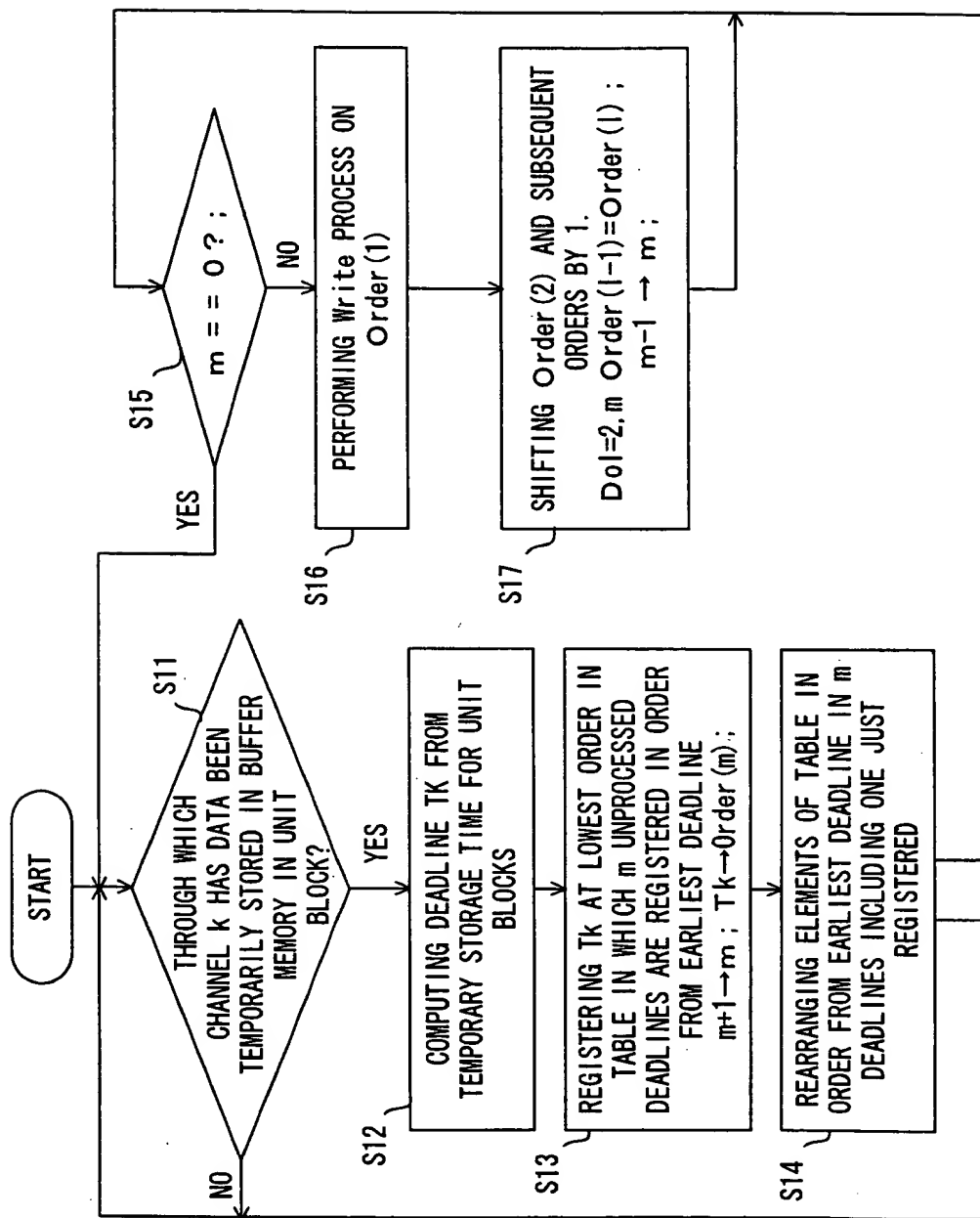


FIG. 18

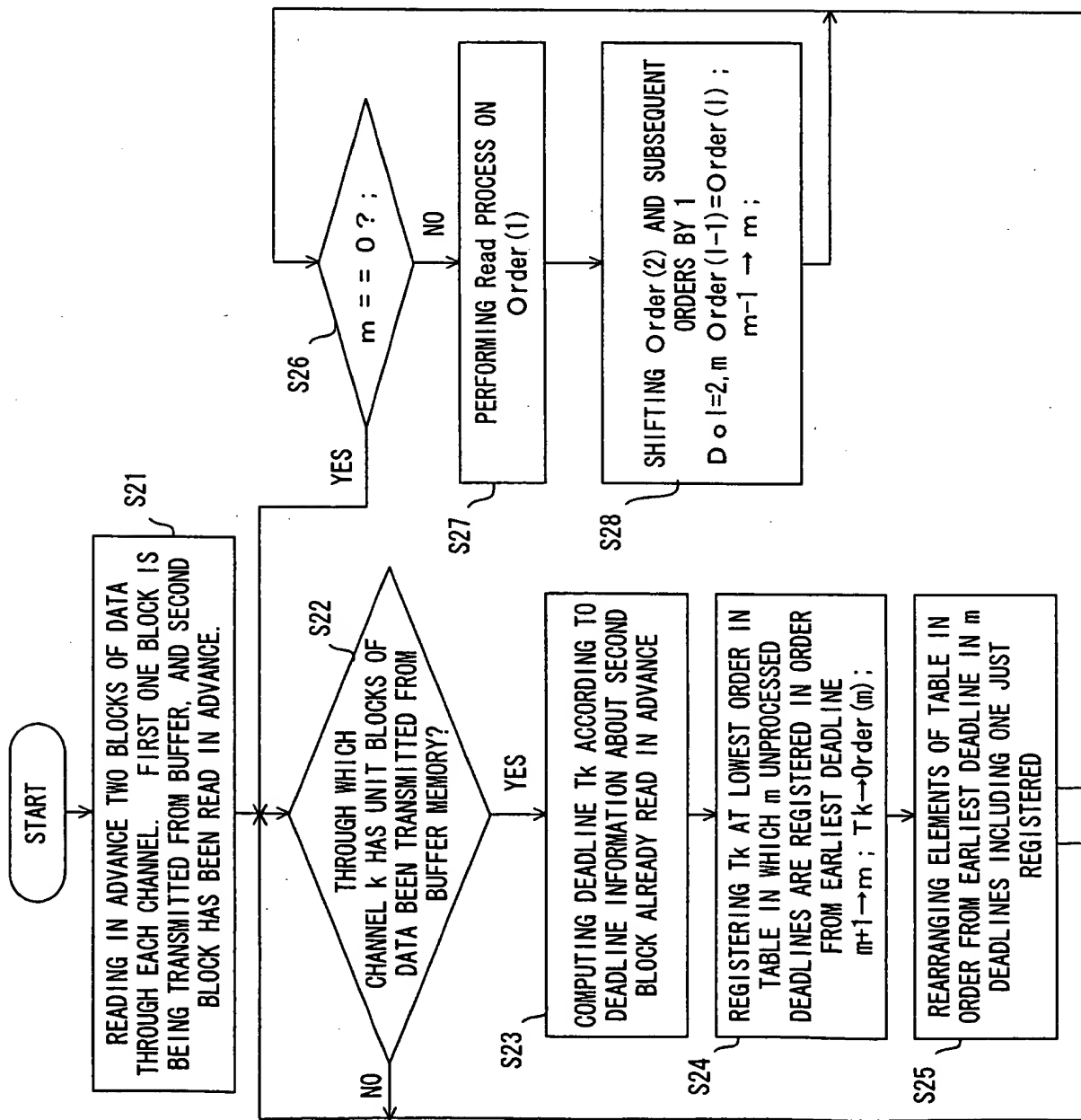


FIG. 19

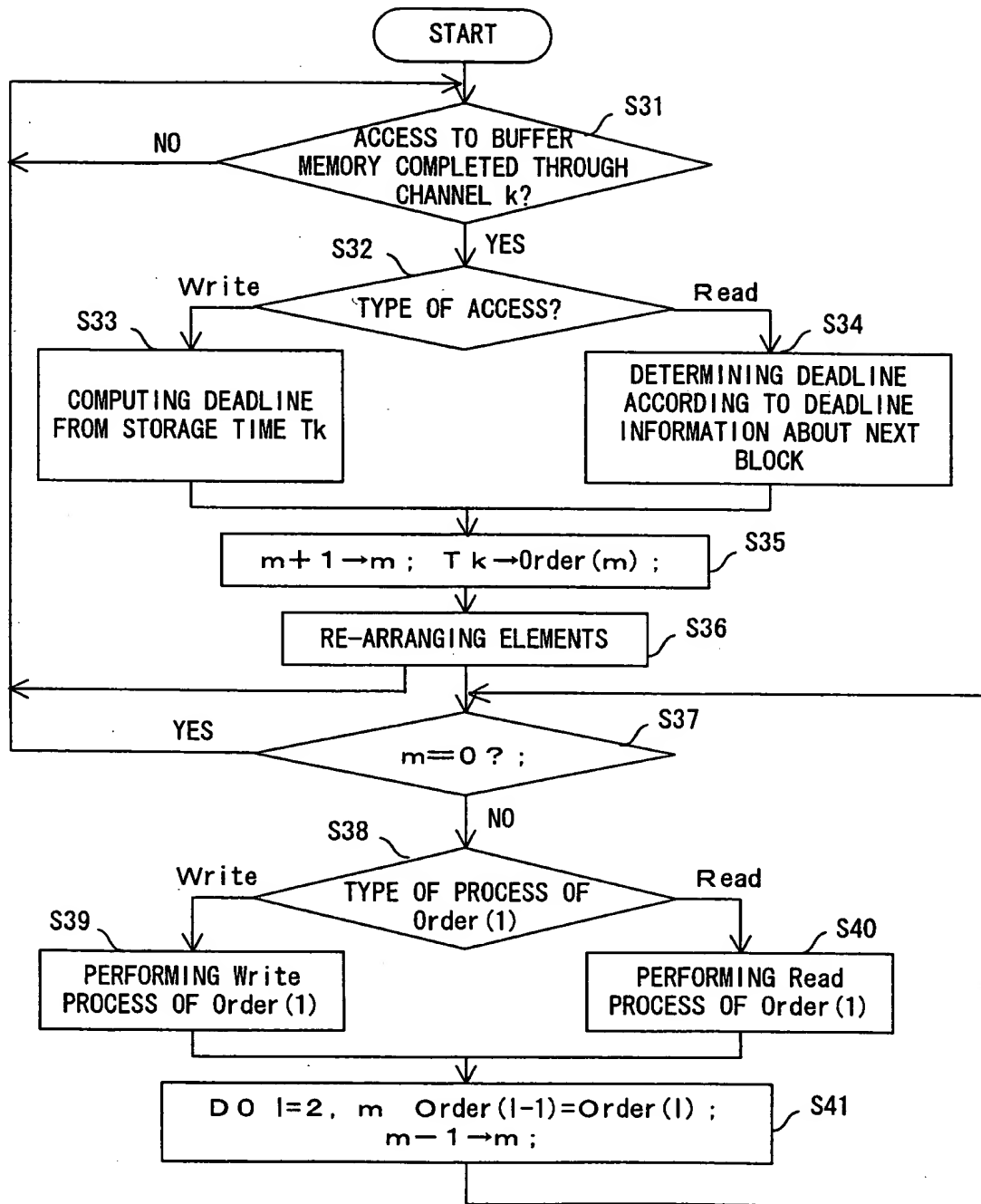


FIG. 20

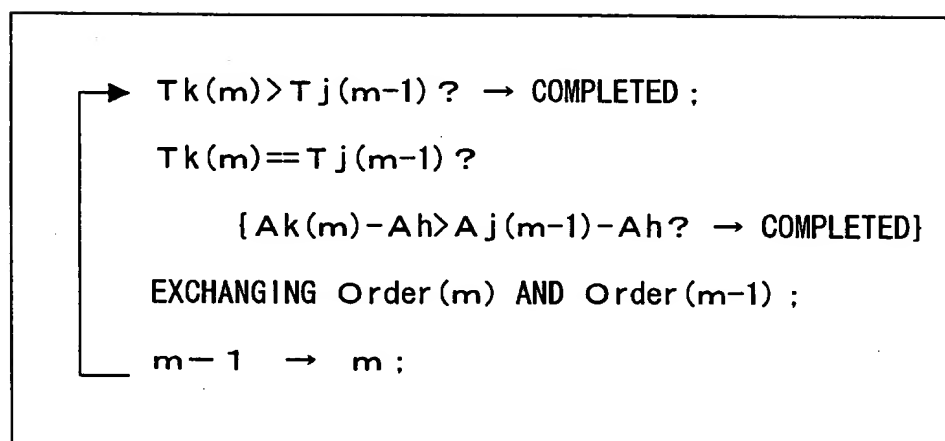


FIG. 21

$T_k(m) > T_j(m/2) ? \rightarrow > T_j(3m/4) ?$

$< T_j(3m/4) ?$

CONTINUING

$T_k(m) < T_j(m/2) ? \rightarrow > T_j(m/4) ?$

$< T_j(m/4) ?$

CONTINUING

INSERTING Order(m) IN ORDER OF DETERMINATION ;

F I G. 2 2

```
graph TD; START([START]) --> S51[S51: BUFFERING DATA AT Read/Write REAL TIME INSTRUCTION THROUGH PLURAL CHANNELS]; S51 --> S52[S52: CHANGING ZONE OF DISK OUTSIDE IN WHICH DATA IS TO BE WRITTEN DEPENDING ON NUMBER OF PROCESSES OF REAL TIME Write INSTRUCTIONS WHEN IT HAS INCREASED]; S52 --> S53[S53: CHANGING ZONE OF DISK INSIDE IN WHICH DATA IS TO BE WRITTEN DEPENDING ON NUMBER OF PROCESSES OF REAL TIME Write INSTRUCTIONS WHEN IT HAS DECREASED]; S53 --> END([END]);
```

FIG. 23

000155-0340
TOTAL: 5051000

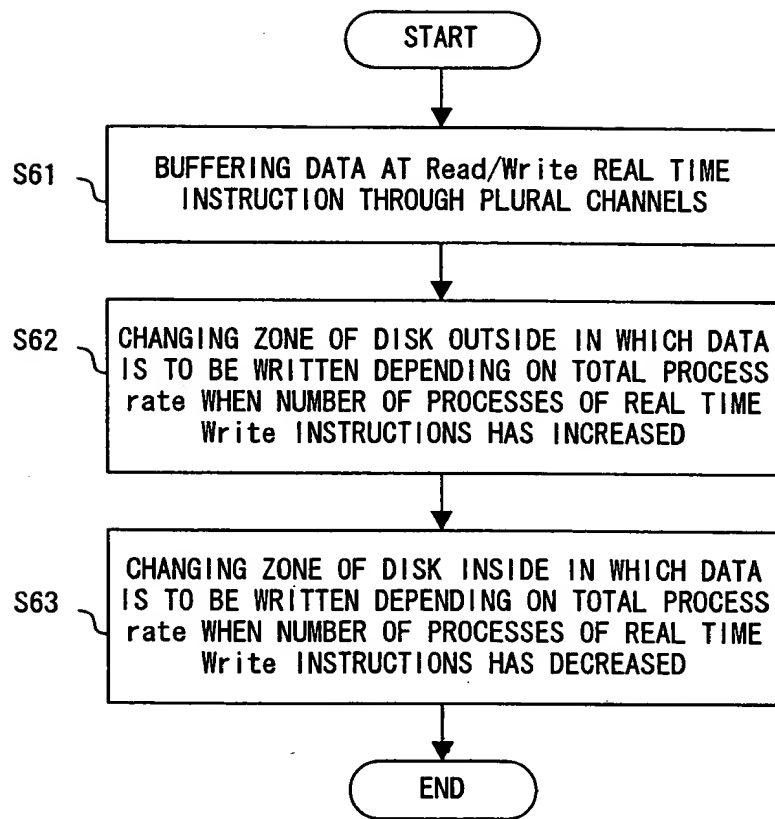


FIG. 24

FIG. 25

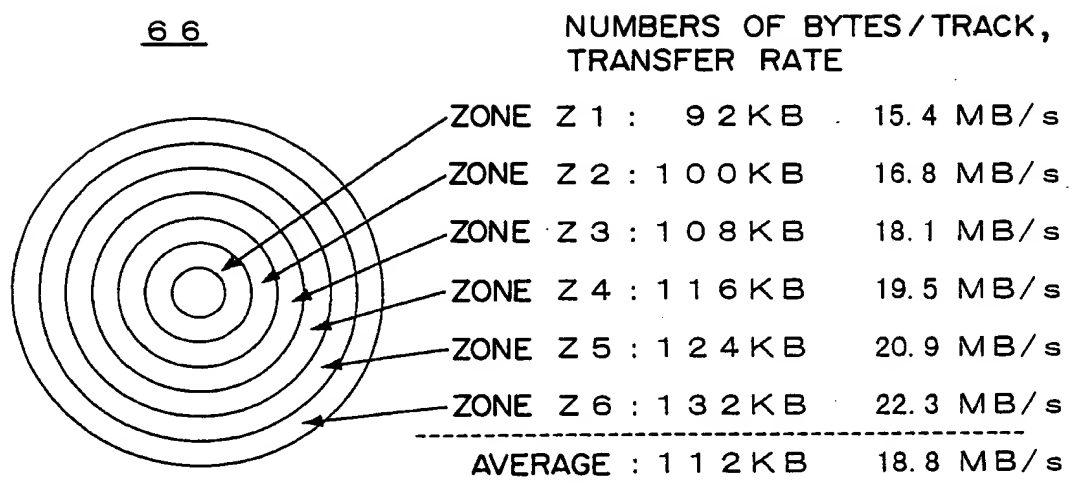


FIG. 25

TOP SECRET

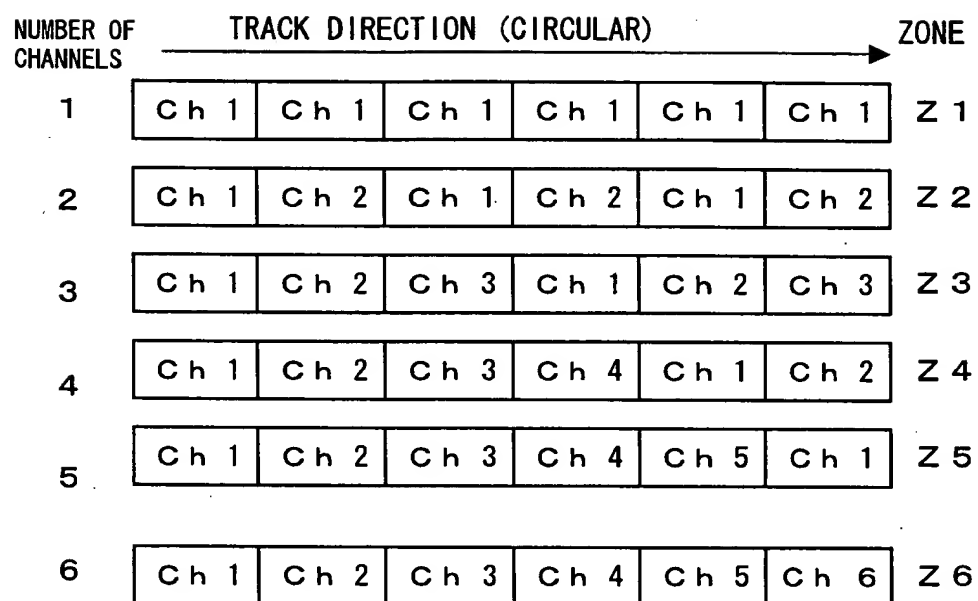


FIG. 26

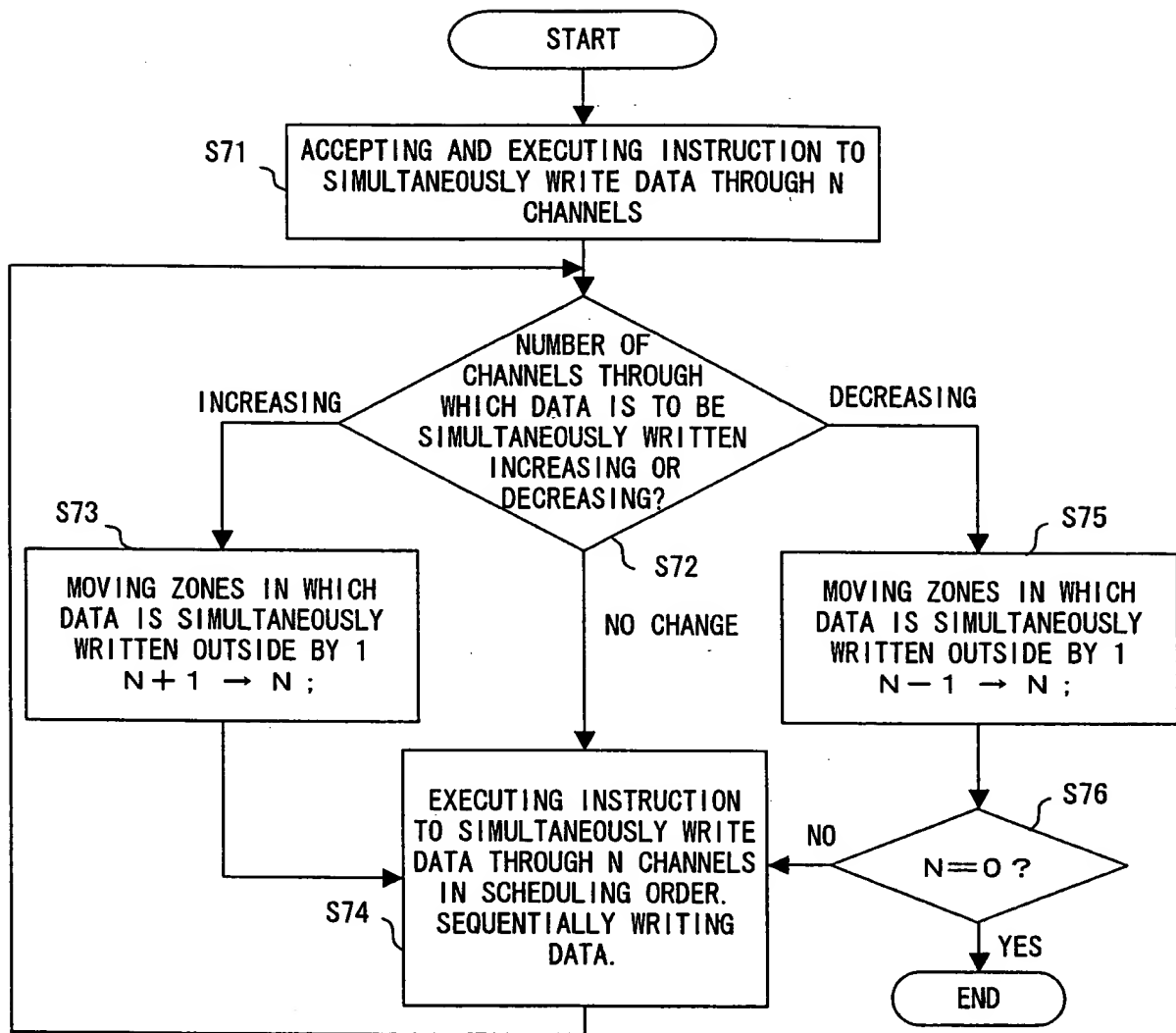


FIG. 27

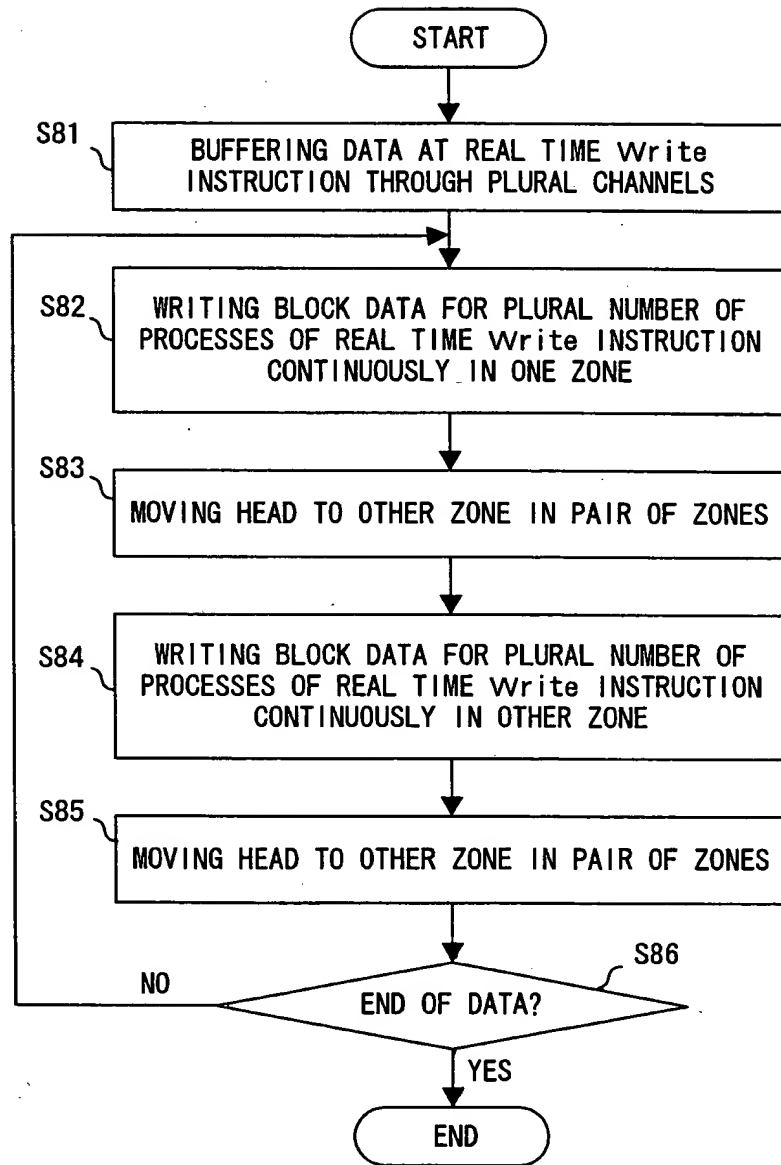


FIG. 28

TOP SECRET

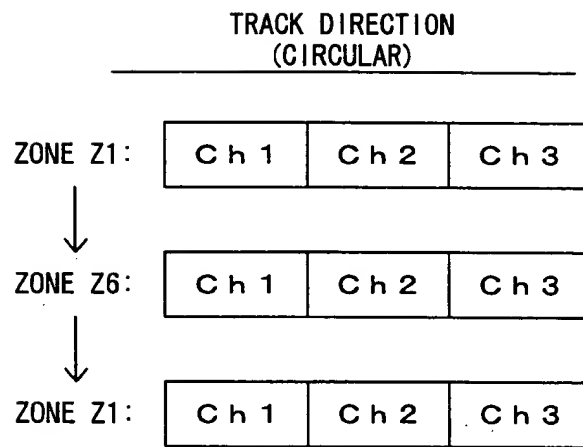


FIG. 29

The block diagram illustrates a portable data processing system (61). The system is composed of several interconnected components:

- System 61:** The overall portable data processing system, represented by a large rectangle.
- Micro-Processor 71:** A component within the system 61, connected to the system bus 65.
- Memory 72:** Another component within the system 61, also connected to the system bus 65.
- System Bus 65:** A central vertical line with double-headed arrows representing the communication bus connecting the internal components and external devices.
- LSI 62:** A Large Scale Integrated circuit connected to the system bus 65. It is also connected to an external interface labeled "IEEE 1394".
- Medium Drive Device 73:** Connected to the system bus 65 and the portable storage medium 74.
- Portable Storage Medium 74:** A storage device connected to the medium drive device 73.

F I G. 30

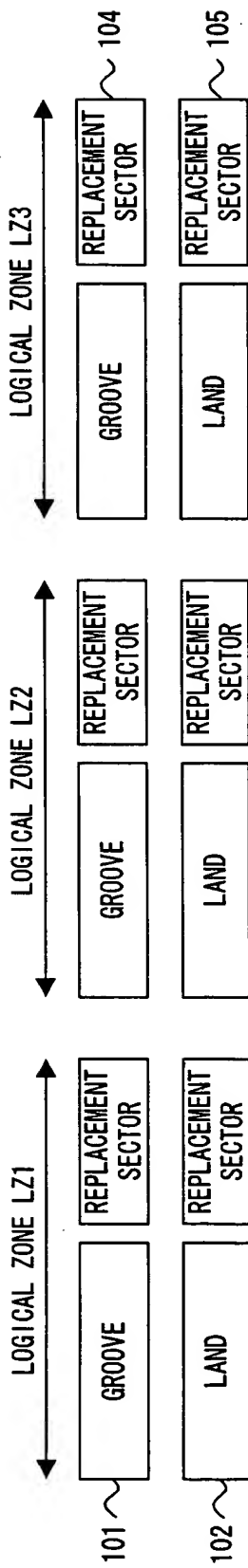


FIG. 31

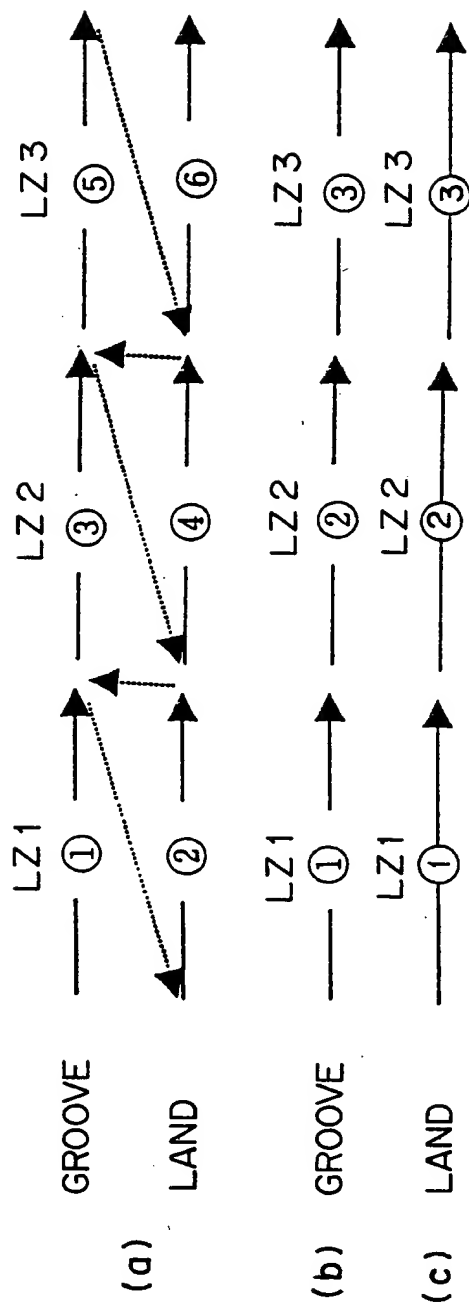


FIG. 32

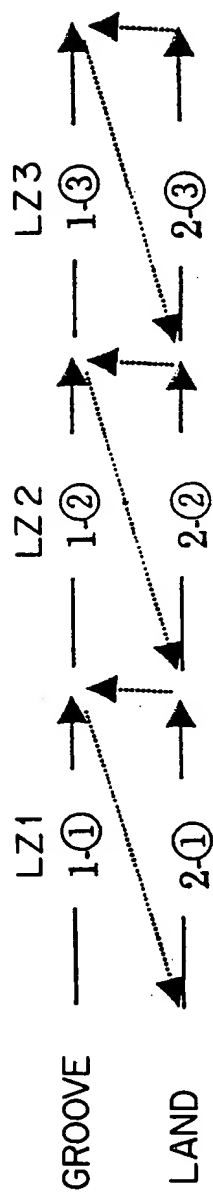


FIG. 33

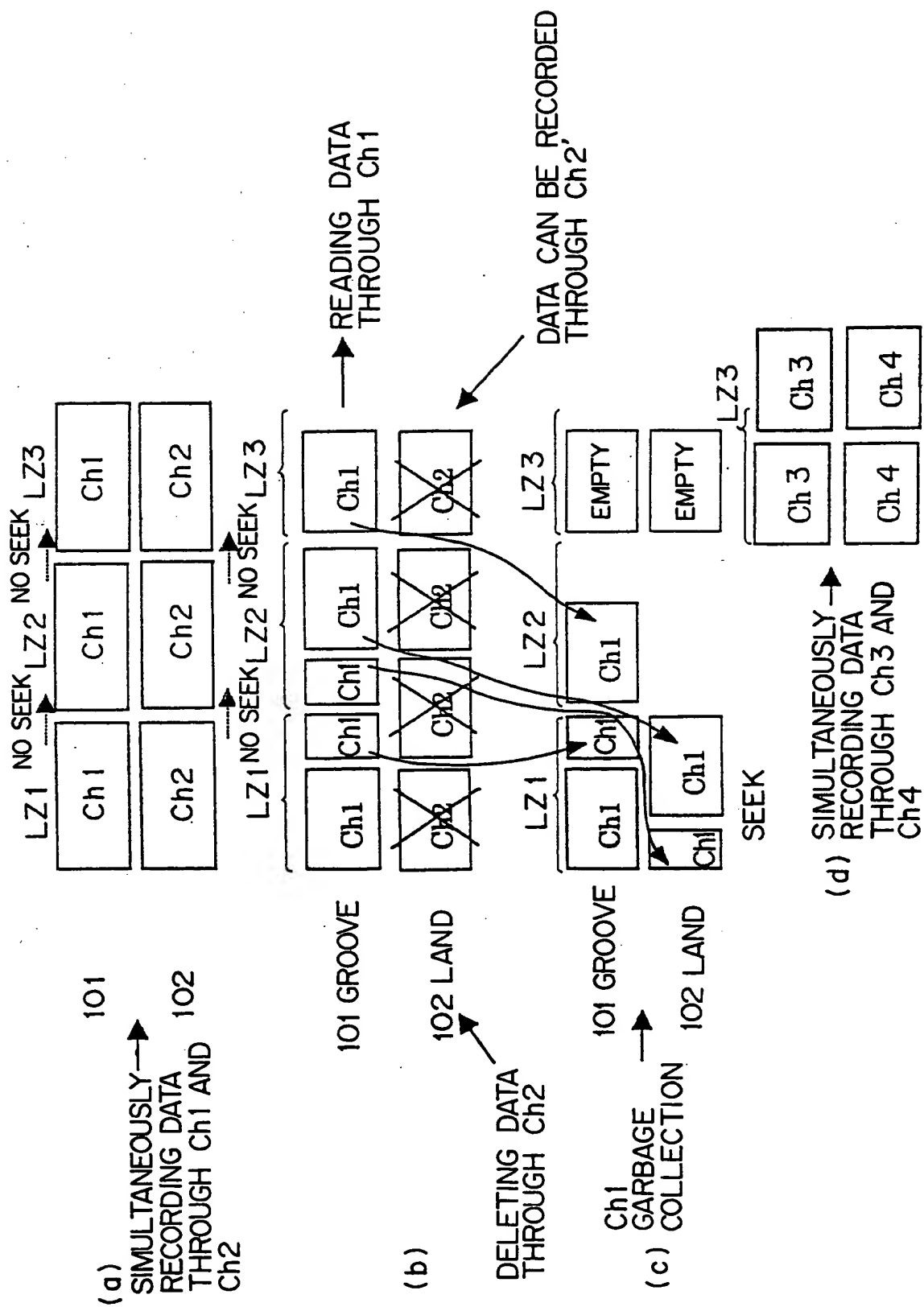


FIG. 34

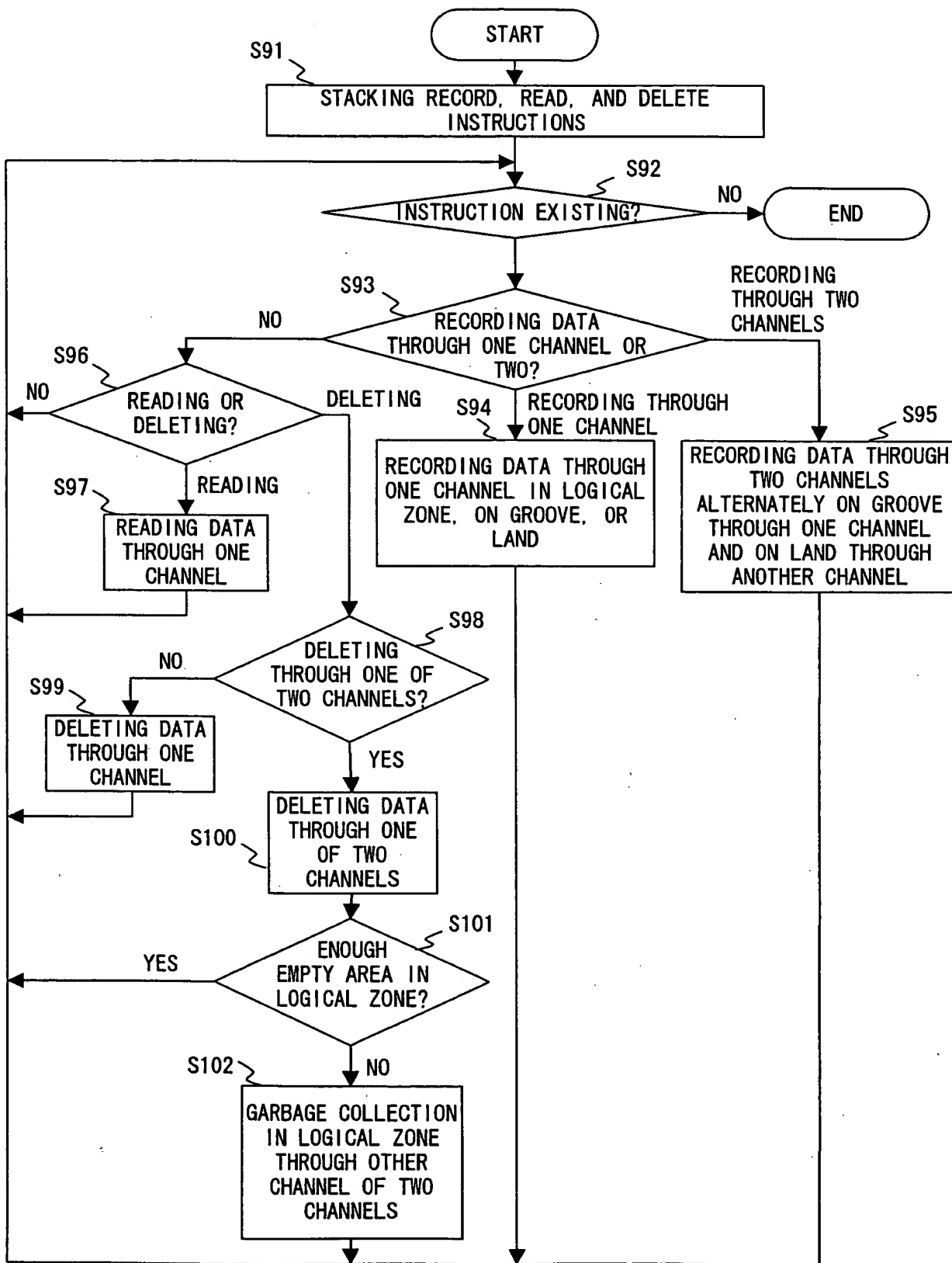


FIG. 35

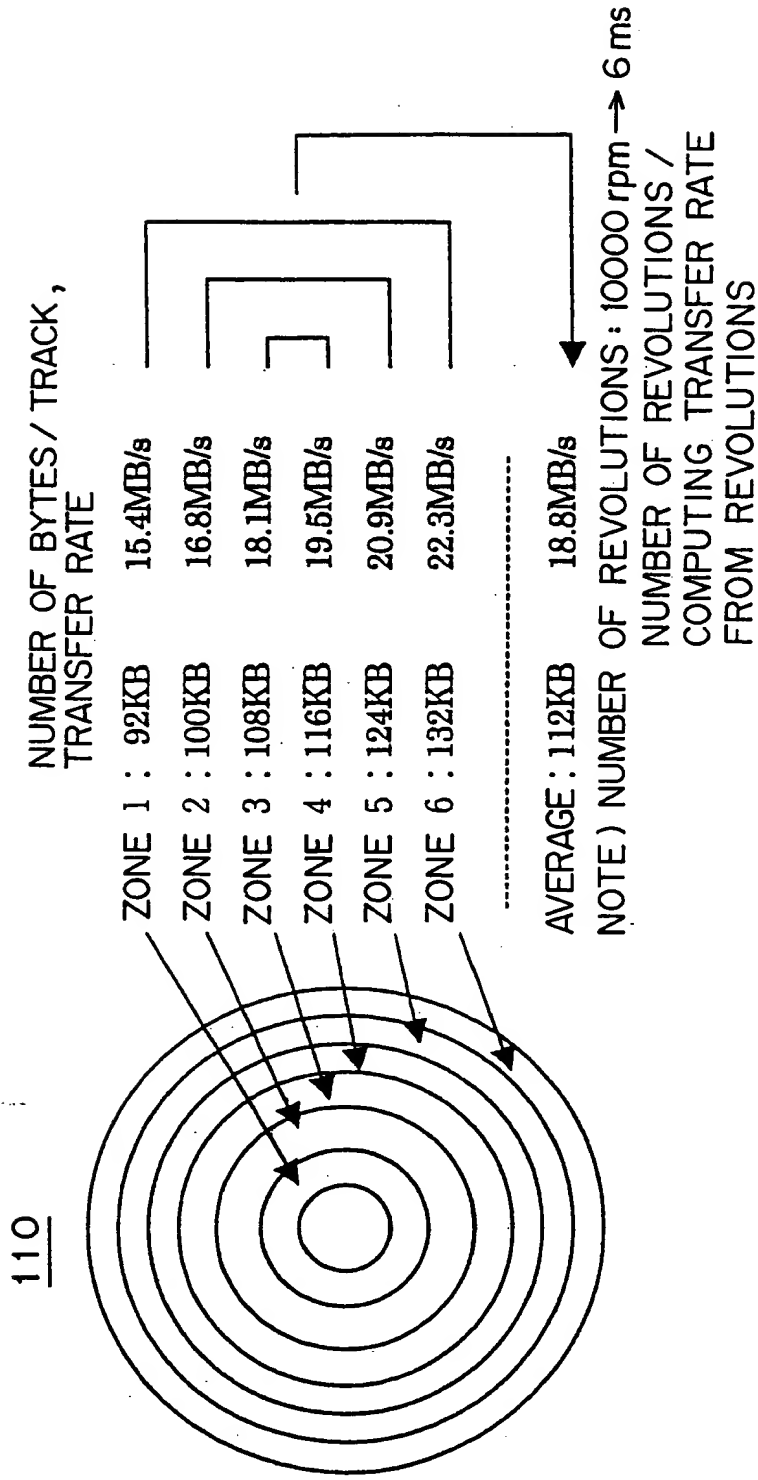


FIG. 36

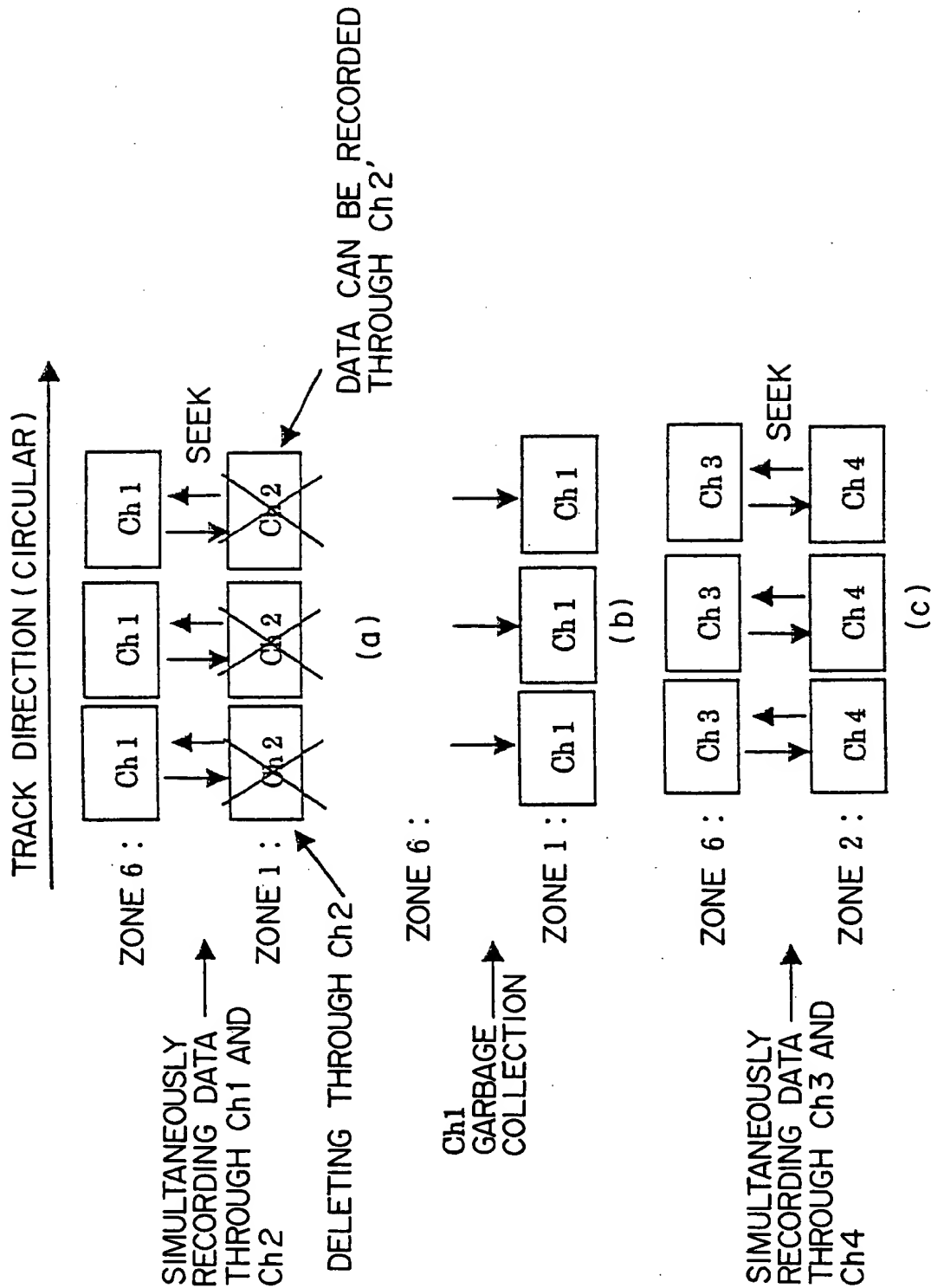


FIG. 37

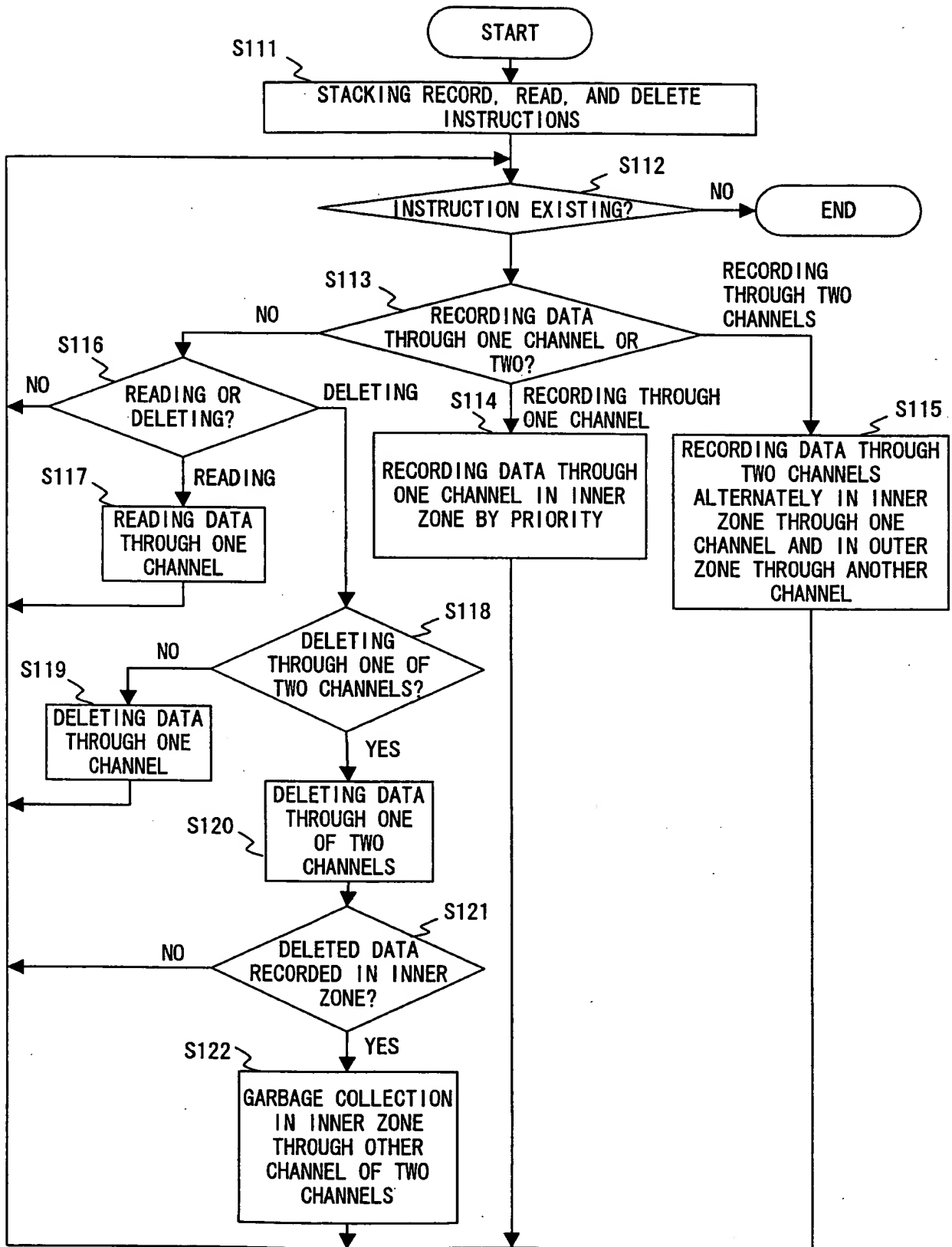
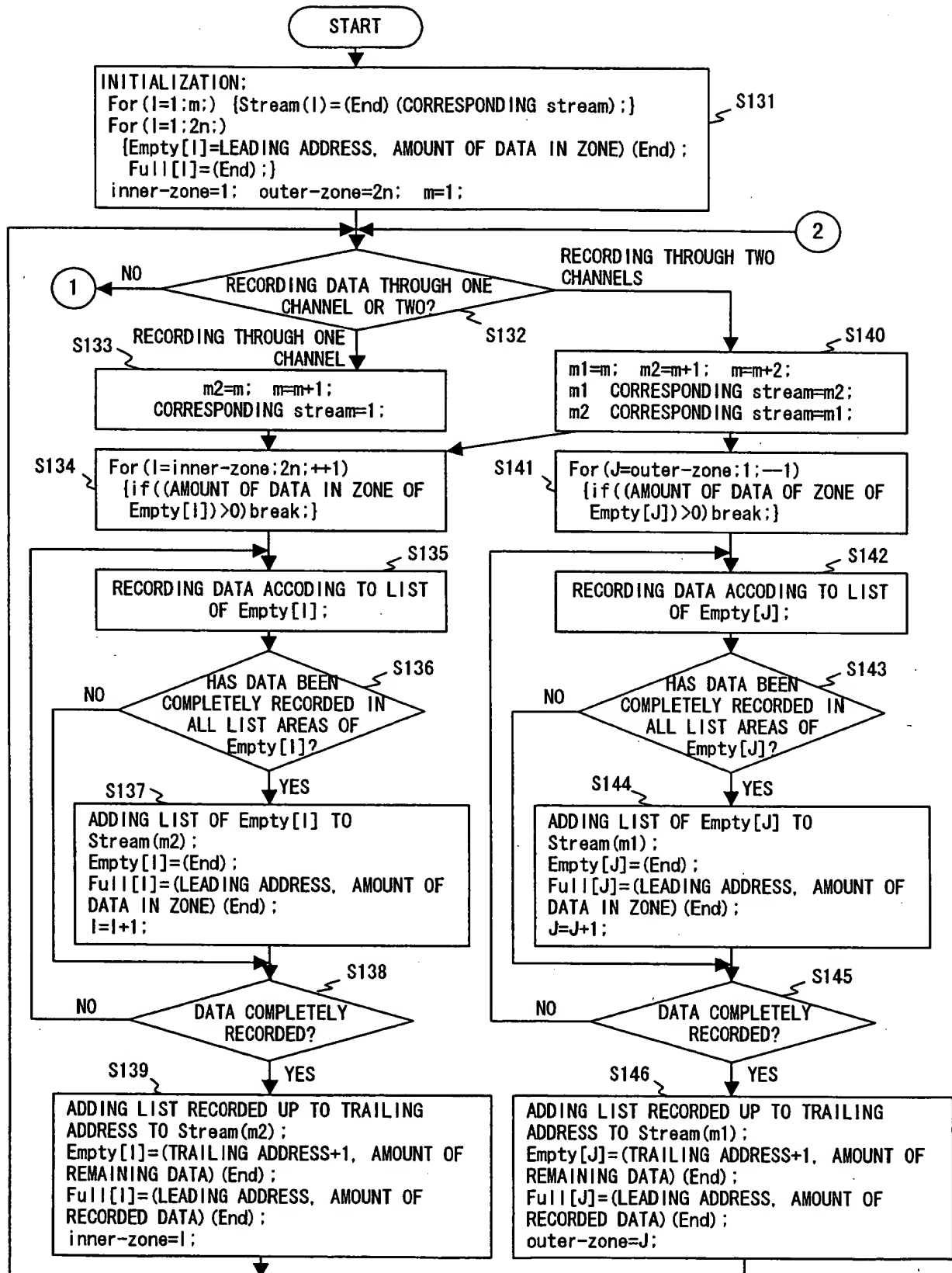


FIG. 38



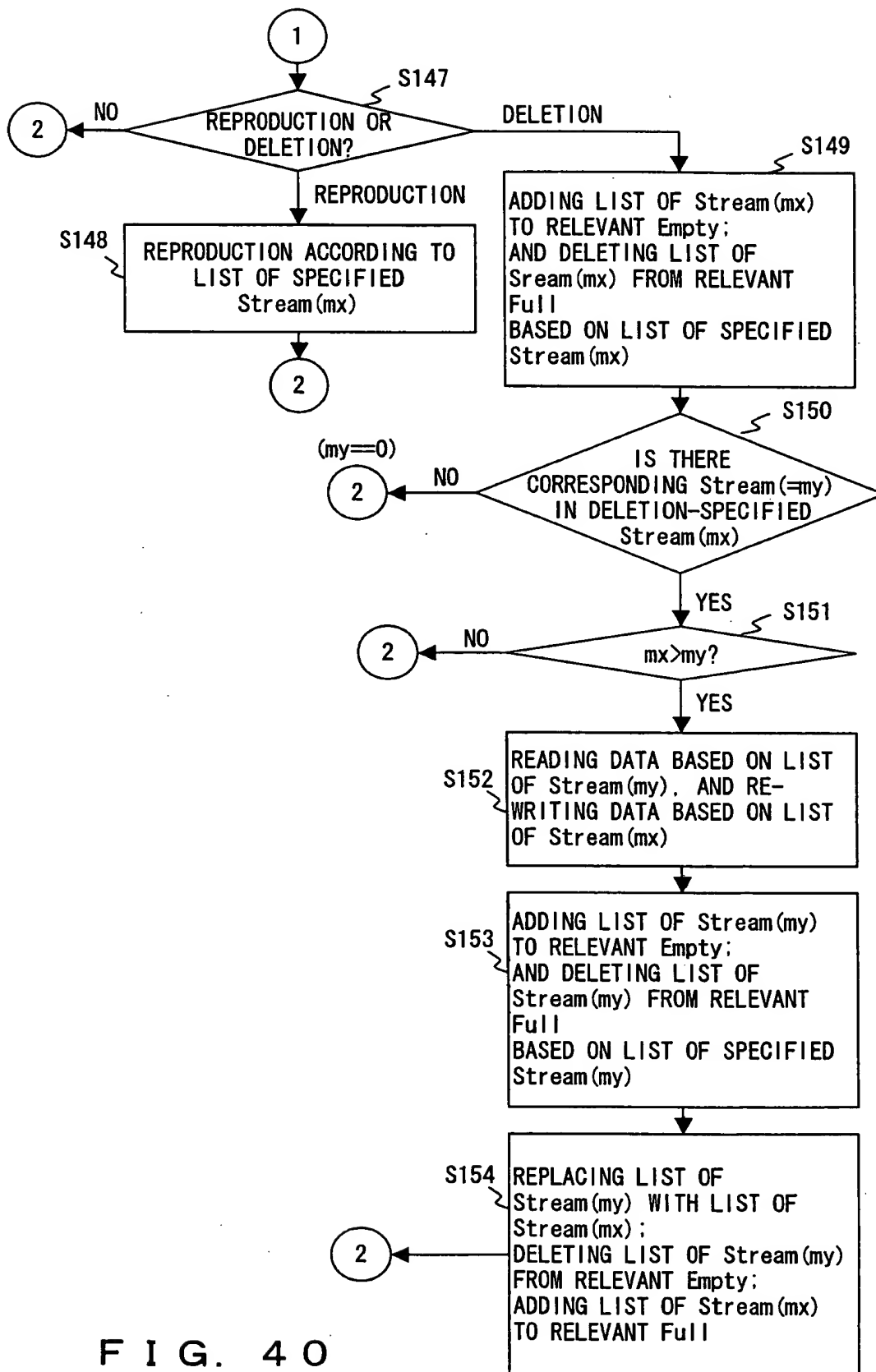


FIG. 40

STREAM LIST	(LEADING ADDRESS, AMOUNT OF DATA) → COMPLETION, CORRESPONDING stream FOR RECORDING DATA THROUGH TWO CHANNELS
Stream(1)	(Add, Data) → (Add, Data) →.....→ End.m?
⋮	⋮
Stream(m)	(Add, Data) → (Add, Data) →.....→ End.m?

FIG. 41

130

INNER/ OUTER	ZONE	EMPTY/FULL LIST	(LEADING ADDRESS, AMOUNT OF DATA) → COMPLETION : LIST STRUCTURE
INNER ZONE	1	Empty[1]	(Add, Data) → (Add, Data) → → End
		Full[1]	(Add, Data) → (Add, Data) → → End
	n	Empty[n]	(Add, Data) → End (INITIAL VALUE)
		Full[n]	End (INITIAL VALUE)
OUTER ZONE	n+1	Empty[n+1]	(Add, Data) → End (INITIAL VALUE)
		Full[n+1]	End (INITIAL VALUE)
	2n	Empty[2n]	(Add, Data) → (Add, Data) → → End
		Full[2n]	(Add, Data) → (Add, Data) → → End

FIG. 42

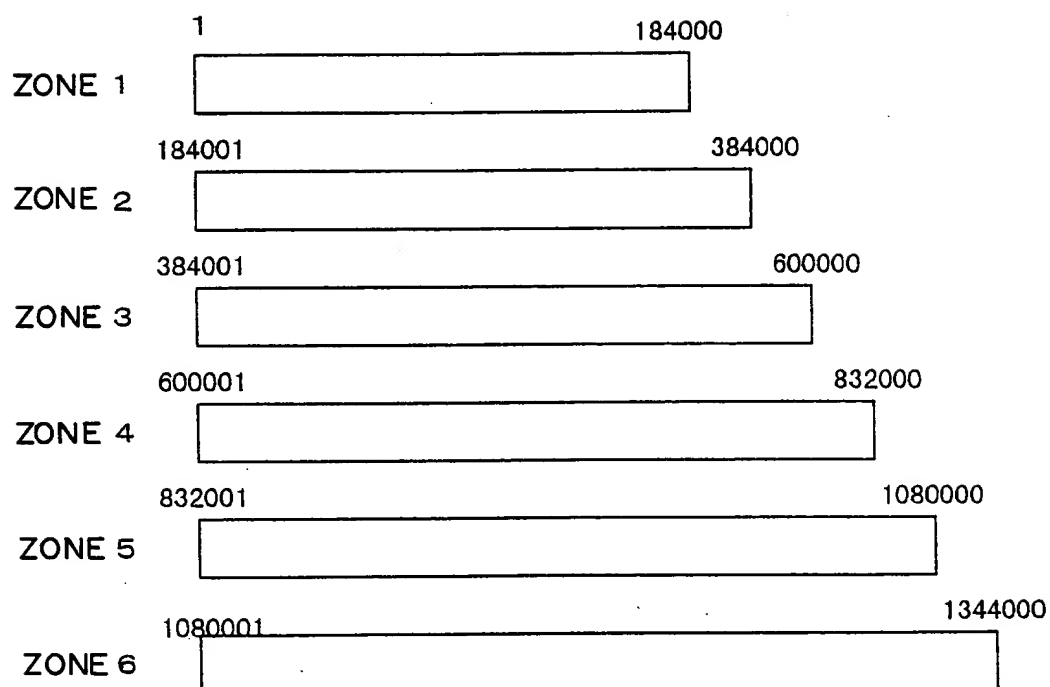
ZONE no.	NUMBER OF BYTES/TRACK	NUMBER OF TRACKS	NUMBER OF SECTORS	SECTOR ADDRESS
1	92KB	1000	184k	1-184000
2	100KB	1000	200k	184000-384000
3	108KB	1000	216k	384001-600000
4	116KB	1000	232k	600001-832000
5	124KB	1000	248k	832001-1080000
6	132KB	1000	264k	1080001-1344000

FIG. 43

	STORAGE ADDRESS	MEANING OF INFORMATION	STORED DATA (LEADING STORAGE DEVICE, AMOUNT OF DATA, NEXT STORAGE ADDRESS)
INITIALIZED AREA	0	End	(0, 0, 0)
	1	Stream(1)	^① (0, 0, 0) ^③ →(1080001, 264000, 19) ^⑤ →(1, 184000, 20)
	2	Stream(2)	^① (0, 0, 0) ^② →(1, 184000, 18) ^④ →(0, 0, 0)
	3	Stream(3)	^① (0, 0, 0) ^⑦ →(1080001, 256000, 22)
	4	Stream(4)	^① (0, 0, 0) ^⑥ →(300001, 84000, 21)
	5	Stream(5)	^① (0, 0, 0)
	6	Empty[1]	^① (1, 184000, 0) ^② →(0, 0, 0) ^④ →(1, 184000, 0) ^⑤ →(0, 0, 0)
	7	Empty[2]	^① (184001, 200000, 0) ^③ →(300001, 84000, 0) ^④ →(184001, 200000, 0) ^⑤ →(300001, 84000, 0) ^⑥ →(0, 0, 0)
	8	Empty[3]	^① (384001, 216000, 0) ^⑦ →(0, 0, 0)
	9	Empty[4]	^① (600001, 232000, 0)
	10	Empty[5]	^① (832001, 248000, 0) ^③ →(876001, 204000, 0) ^⑤ →(832001, 248000, 0) ^⑦ →(876001, 204000, 0)
	11	Empty[6]	^① (1080001, 256000, 0) ^③ →(0, 0, 0) ^⑤ →(1080001, 256000, 0) ^⑦ →(0, 0, 0)
	12	Full[1]	^① (1, 0, 0) ^② →(1, 184000, 0) ^④ →(1, 0, 0) ^⑤ →(1, 184000, 0)
	13	Full[2]	^① (184001, 0, 0) ^③ →(184001, 116000, 0) ^④ →(184001, 0, 0) ^⑤ →(184001, 11600, 0) ^⑥ →(184001, 200000, 0)
	14	Full[3]	^① (384001, 0, 0) ^⑦ →(384001, 216000, 0)
	15	Full[4]	^① (600001, 0, 0)
	16	Full[5]	^① (832001, 0, 0) ^③ →(832001, 44000, 0) ^⑤ →(832001, 0, 0) ^⑦ →(832001, 44000, 0)
	17	Full[6]	^① (1080001, 0, 0) ^③ →(1080001, 256000, 0) ^⑥ →(1080001, 84001, 0) ^⑦ →(1080001, 256000, 0)
EXTENSION AREA	18	Z ₁ →Z ₂	(184001, 0, 0) ^③ →(184001, 116000, 0)
	19	Z ₆ →Z ₅	(832001, 0, 0) ^③ →(832001, 44000, 0)
	20	Z ₁ →Z ₂	(184001, 0, 0) ^⑤ →(184001, 116000, 0)
	21	Z ₂ →Z ₃	(384001, 0, 0) ^⑦ →(384001, 216000, 0)
	22	Z ₆ →Z ₅	(832001, 0, 0) ^⑦ →(832001, 44000, 0)
	23		
	24		
	25		
	26		
	27		

FIG. 44

10110 5051000



(1) INITIALIZATION STATUS

FIG. 45

TOP SECRET

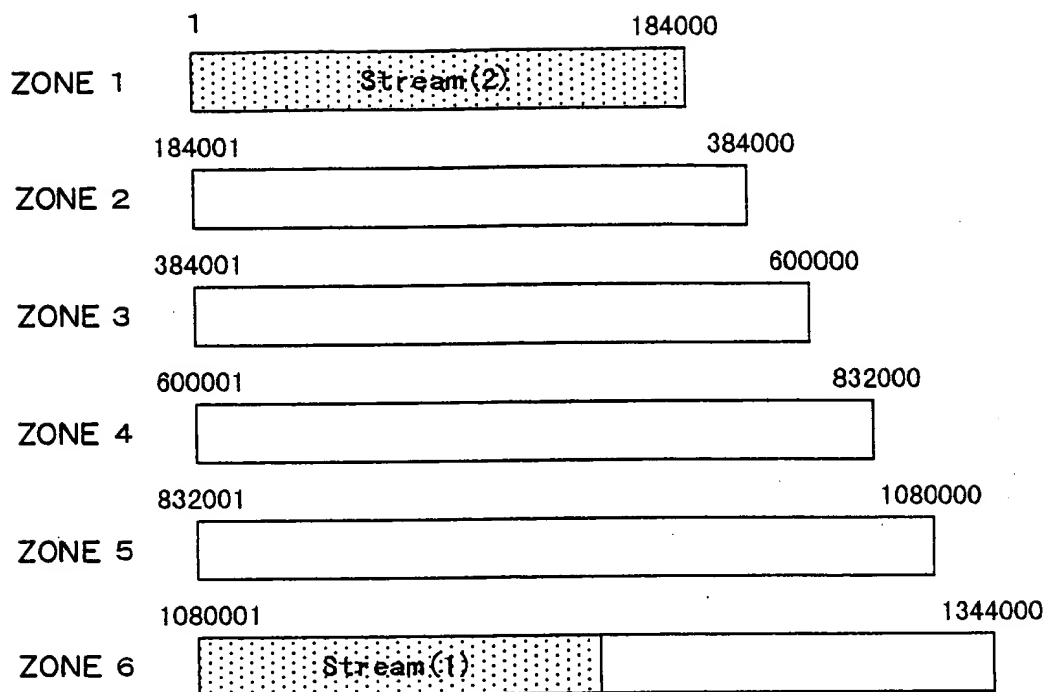


FIG. 46

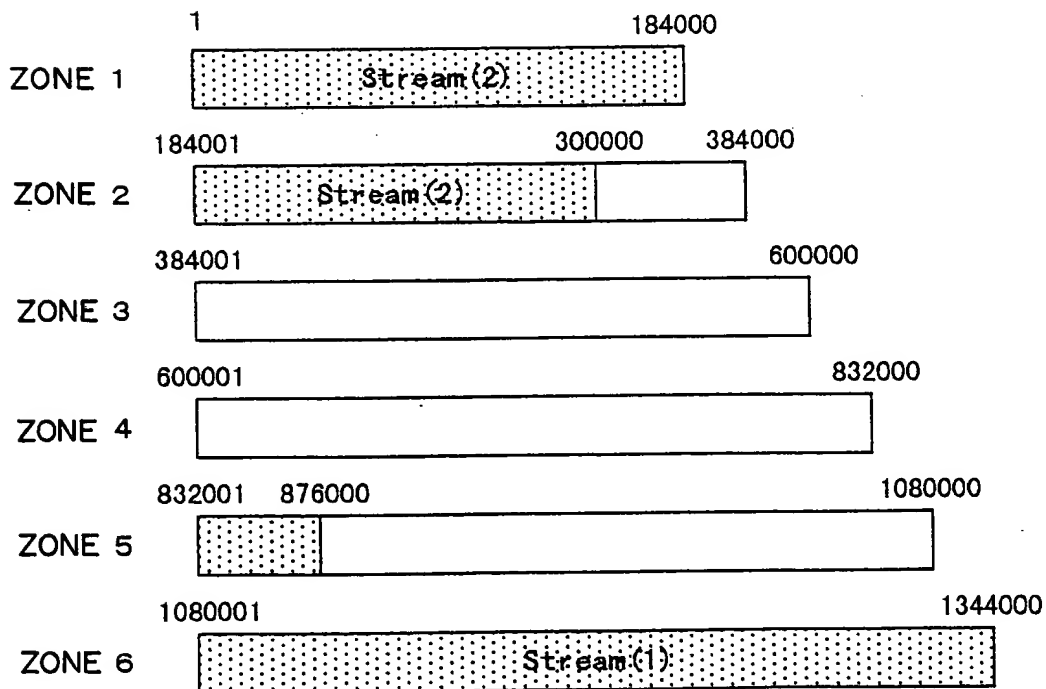


FIG. 47

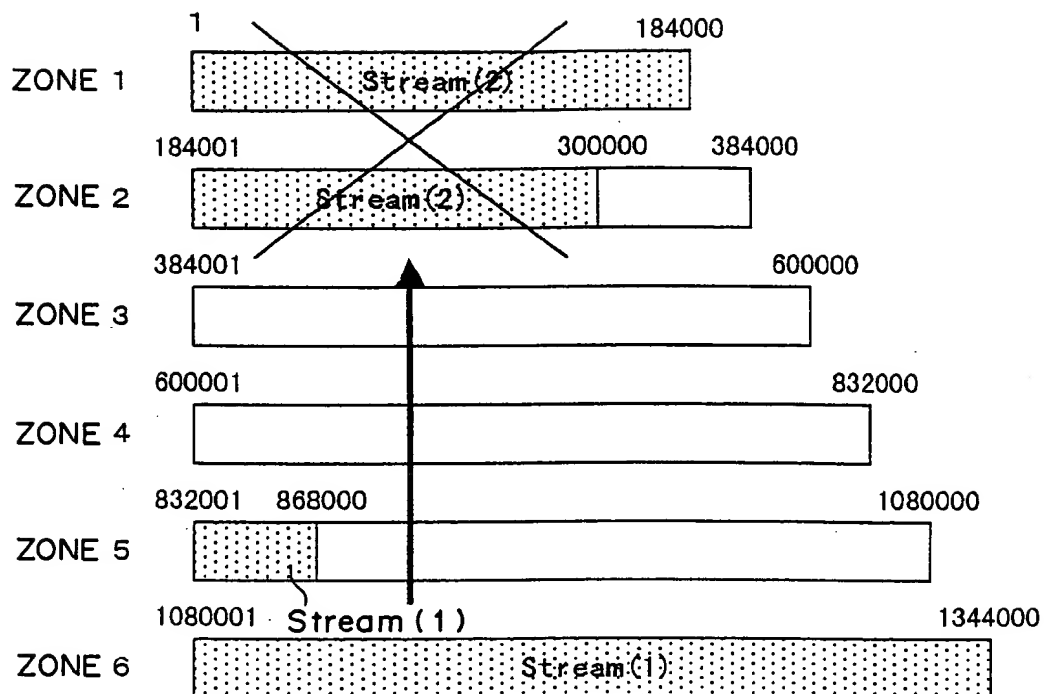


FIG. 48

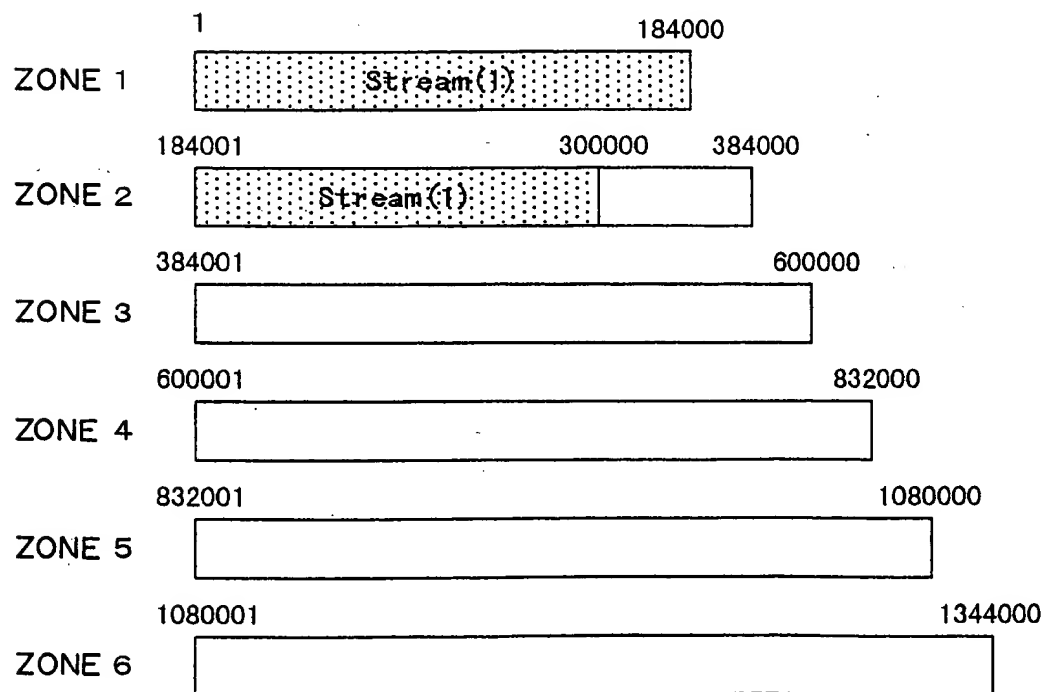


FIG. 49

1 184000

ZONE 1 Stream(1)

184001 300000 384000

ZONE 2 Stream(1) Stream(4)

384001 600000

ZONE 3

600001 832000

ZONE 4

832001 1080000

ZONE 5

1080001 1164001 1344000

ZONE 6 Stream(3)

1 184000

ZONE 1 Stream(1)

184001 300000 384000 Stream(4)

ZONE 2 Stream(1)

384001 600000 Stream(4)

ZONE 3

600001 832000

ZONE 4

832001 876000 1080000

ZONE 5 Stream(3)

1080001 1344000 Stream(3)

ZONE 6

FIG. 51

FIG. 52

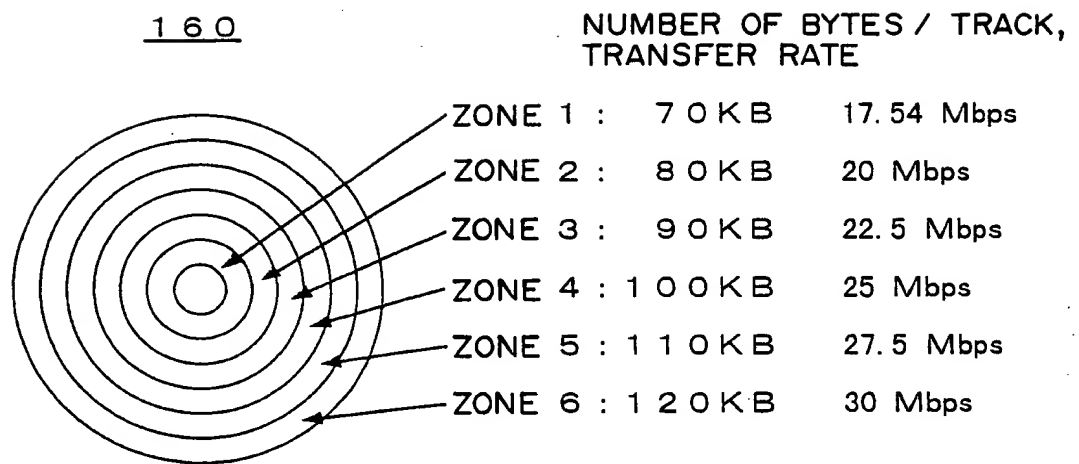
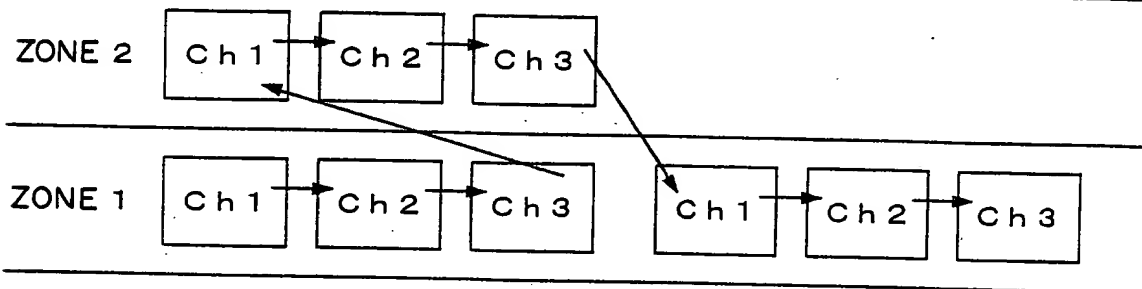


FIG. 52

STEP 1 : DISTRIBUTING AND RECORDING
DATA IN ZONES 1 AND 2

← $(17.5+20)/2 > 18$



(a)

STEP 2: RECORDING

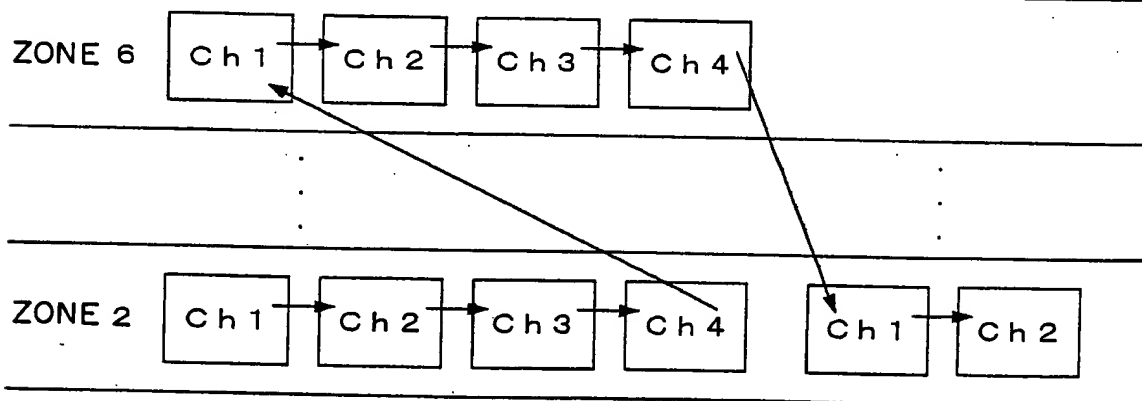
DATA IN ZONE 1 ← $17.5 > 12$



(b)

STEP 3 : DISTRIBUTING AND RECORDING
DATA IN ZONES 2 AND 6

← $(20+30)/2 > 24$



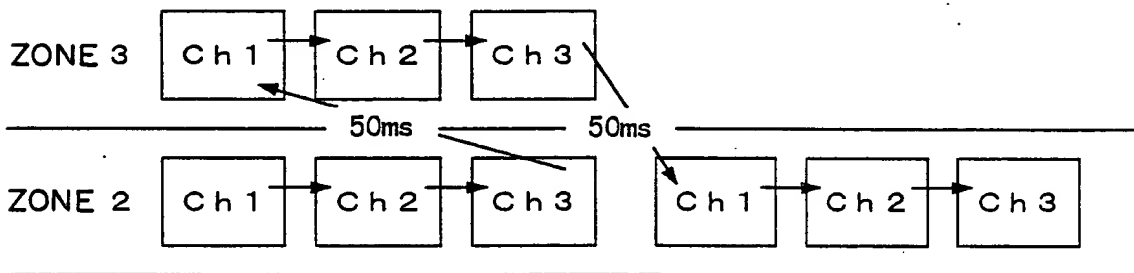
(c)

FIG. 53

FIG. 53

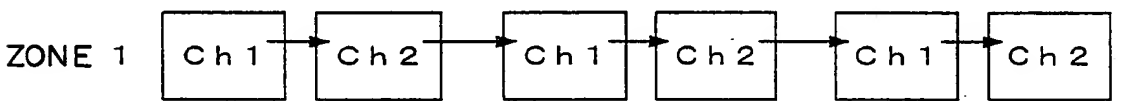
DATA IN ZONES 2 AND 3

$$\leftarrow (20 + 22.5) \times 0.9 / 2 > 18$$



(a)

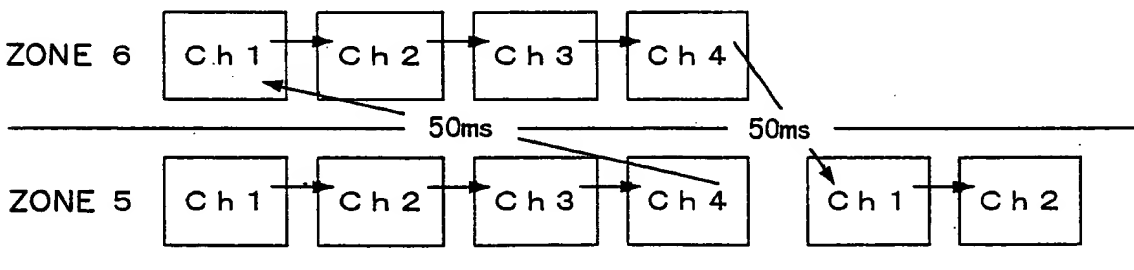
STEP 2: RECORDING
DATA IN ZONE 1 $\leftarrow 17.5 > 12$



(b)

STEP 3 : DISTRIBUTING AND RECORDING DATA IN ZONE 5 AND 6

$$\leftarrow (27.5 + 30) \times 0.9 / 2 > 24$$



(c)

FIG. 54

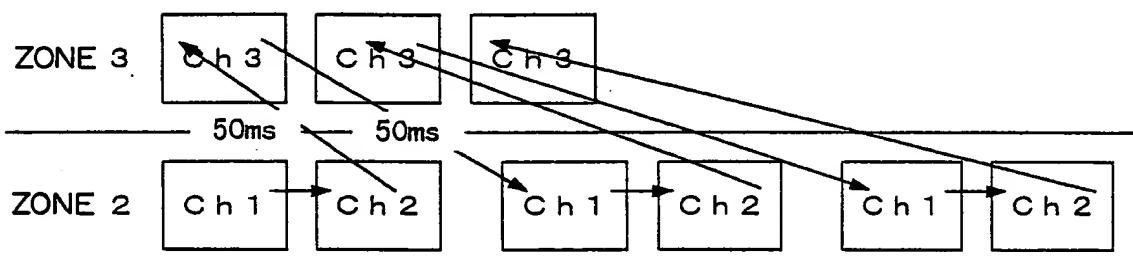
FIG. 55

STEP 1 : DISTRIBUTING AND RECORDING

DATA IN ZONES 2 AND 3

$$\leftarrow (20 + 2/3 + 22.5 \times 1/3) \times 0.9 > 18$$

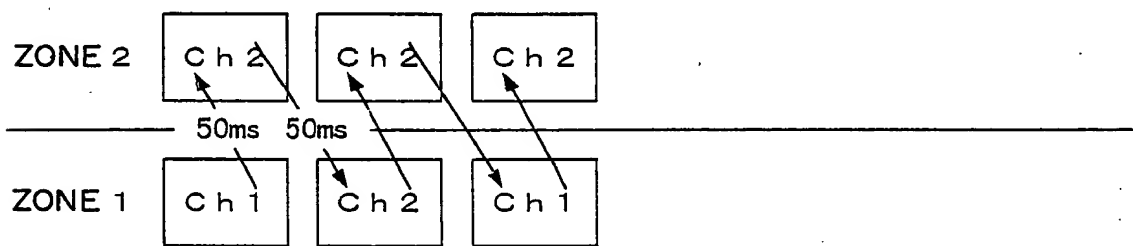
DISTRIBUTING DATA ALTERNATELY THROUGH 1ch AND 2ch



(a)

STEP 2 : RECORDING DATA
IN ZONES 1 AND 2

$$\leftarrow 17.5/2 + 20/2 \times 0.9 > 12$$

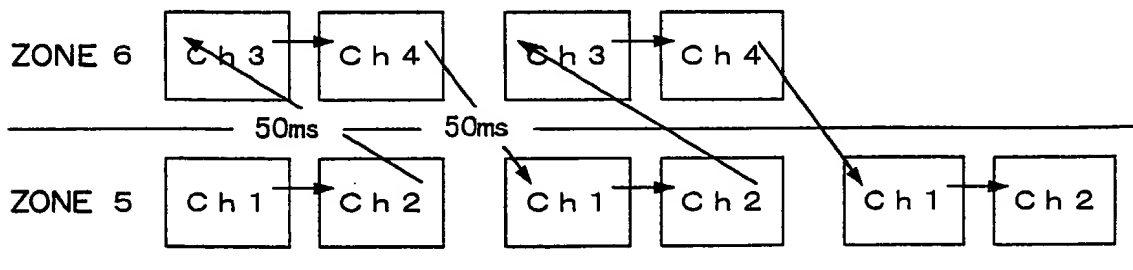


(b)

STEP 3 : DISTRIBUTING AND RECORDING
DATA IN ZONES 5 AND 6

$$\leftarrow (27.5 \times 2/4 + 30 \times 2/4) \times 0.9 > 24$$

ALTERNATELY DISTRIBUTING DATA IN TWO CHANNEL UNITS



(c)

FIG. 55

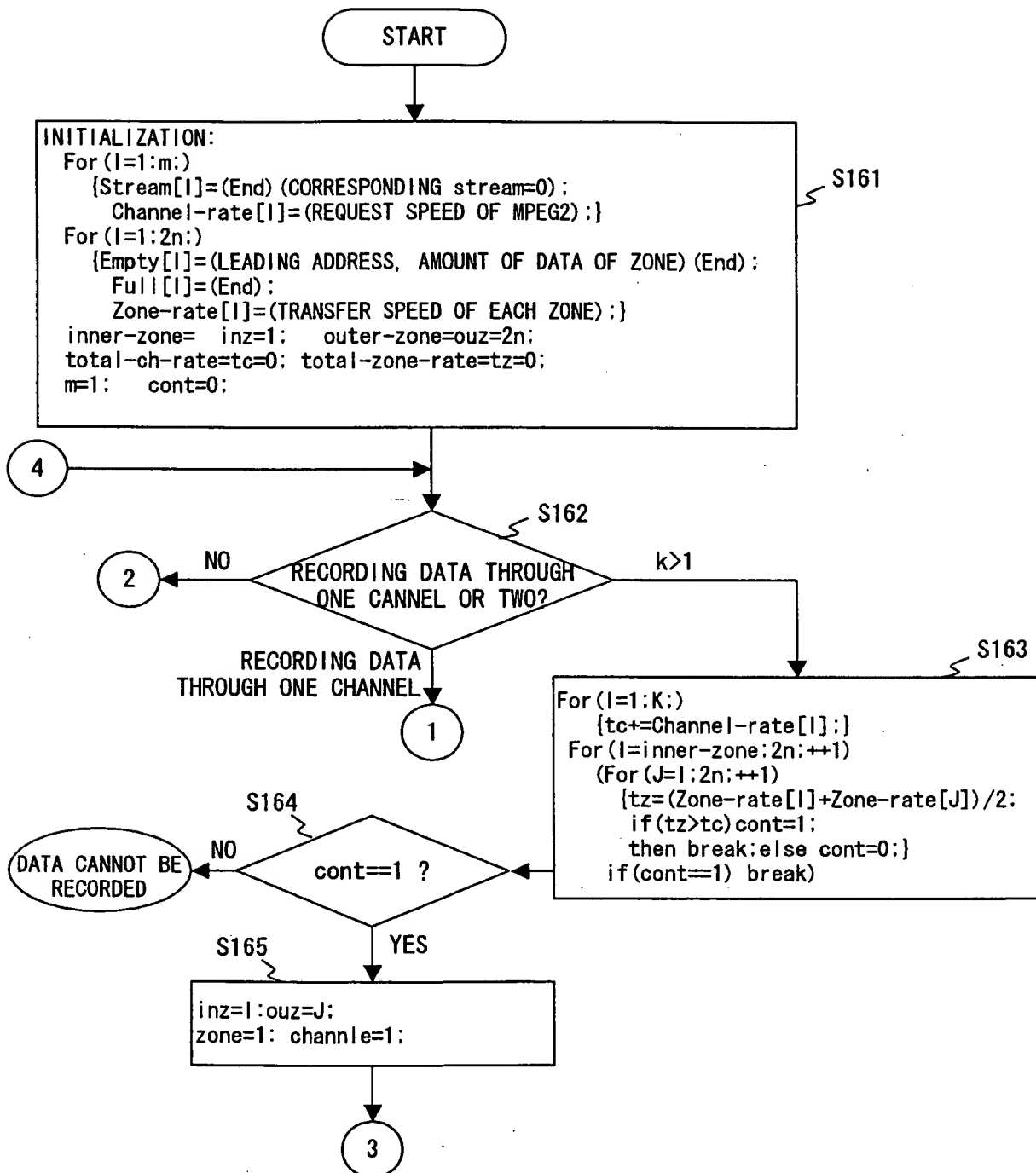
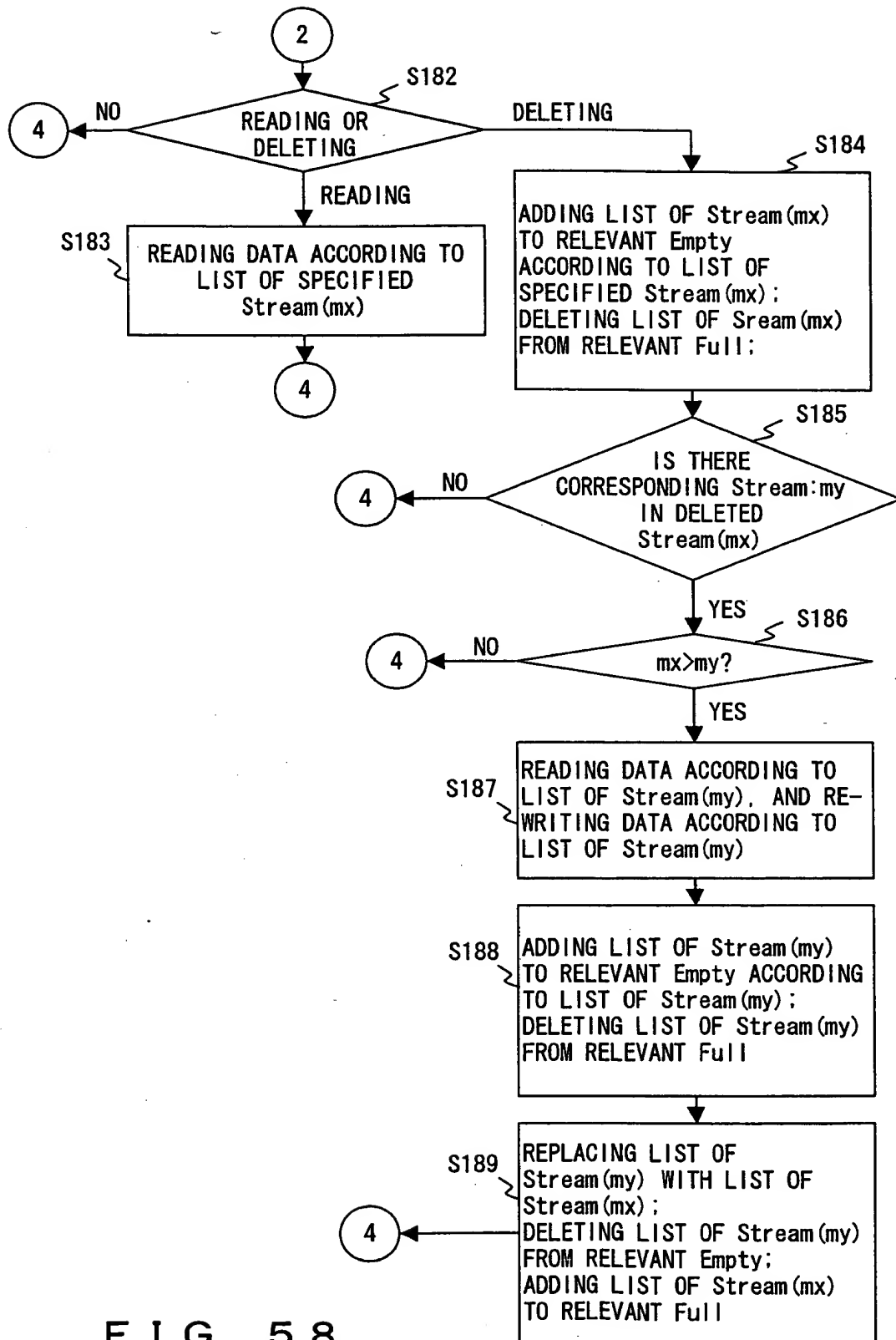


FIG. 56

```

graph TD
    3((3)) -- S166 --> S165[For (I=inz:2n;++1)  
{if(AMOUNT OF DATA IN ZONE OF  
Empty[I])>0)  
break:}]
    S165 -- S167 --> S166a[RECORDING ONE BLOCK THROUGH  
CHANNEL(channel) IN ZONE I ACCORDING  
TO LIST OF Empty[I];  
ADDING LIST OF ONE BLOCK OF Empty[I]  
TO Stream(channel);  
Empty[I]=(TRAILING ADDRESS+1, AMOUNT  
OF REMAINING DATA) (End);  
Full[I]=(LEADING ADDRESS, AMOUNT OF  
RECORDED DATA) (End);  
channel++;]
    S166a --> S167b{channel > K ?}
    S167b -- NO -- S166
    S167b -- YES -- S169[channel=1;]
    S169 --> S170{HAS DATA OF  
LIST AREA OF Empty[I]  
BEEN COMPLETELY  
RECORDED?}
    S170 -- NO -- S166a
    S170 -- YES -- S171[Empty[I]=(End);  
Full[I]=(LEADING ADDRESS, AMOUNT OF  
ZONE) (End);  
I=I+1;]
    S171 --> S172{HAS DATA COMPLETELY  
BEEN RECORDED?}
    S172 -- NO -- S166a
    S172 -- YES -- S173[inner-zone=I;]
    S173 --> 4((4))
    
    3 -- S174 --> S173a[For (J=ouz:2n;++1)  
{if(AMOUNT OF DATA IN ZONE OF  
Empty[J])>0)  
break:}]
    S173a -- S175 --> S174a[RECORDING ONE BLOCK THROUGH  
CHANNEL(channel) IN ZONE J ACCORDING  
TO LIST OF Empty[J];  
ADDING LIST OF ONE BLOCK OF Empty[J]  
TO Stream(channel);  
Empty[J]=(TRAILING ADDRESS+1, AMOUNT  
OF REMAINING DATA) (End);  
Full[J]=(LEADING ADDRESS, AMOUNT OF  
RECORDED DATA) (End);  
channel++;]
    S174a --> S175b{channel > K ?}
    S175b -- NO -- S173a
    S175b -- YES -- S177[channel=1;]
    S177 --> S178{HAS DATA OF  
LIST AREA OF Empty[J]  
BEEN COMPLETELY  
RECORDED?}
    S178 -- NO -- S174a
    S178 -- YES -- S179[Empty[J]=(End);  
Full[J]=(LEADING ADDRESS, AMOUNT OF  
ZONE) (End);  
J=J+1;]
    S179 --> S180{HAS DATA COMPLETELY  
BEEN RECORDED?}
    S180 -- NO -- S174a
    S180 -- YES -- S181[outer-zone=J;]
    S181 --> 4
  
```

FIG. 57



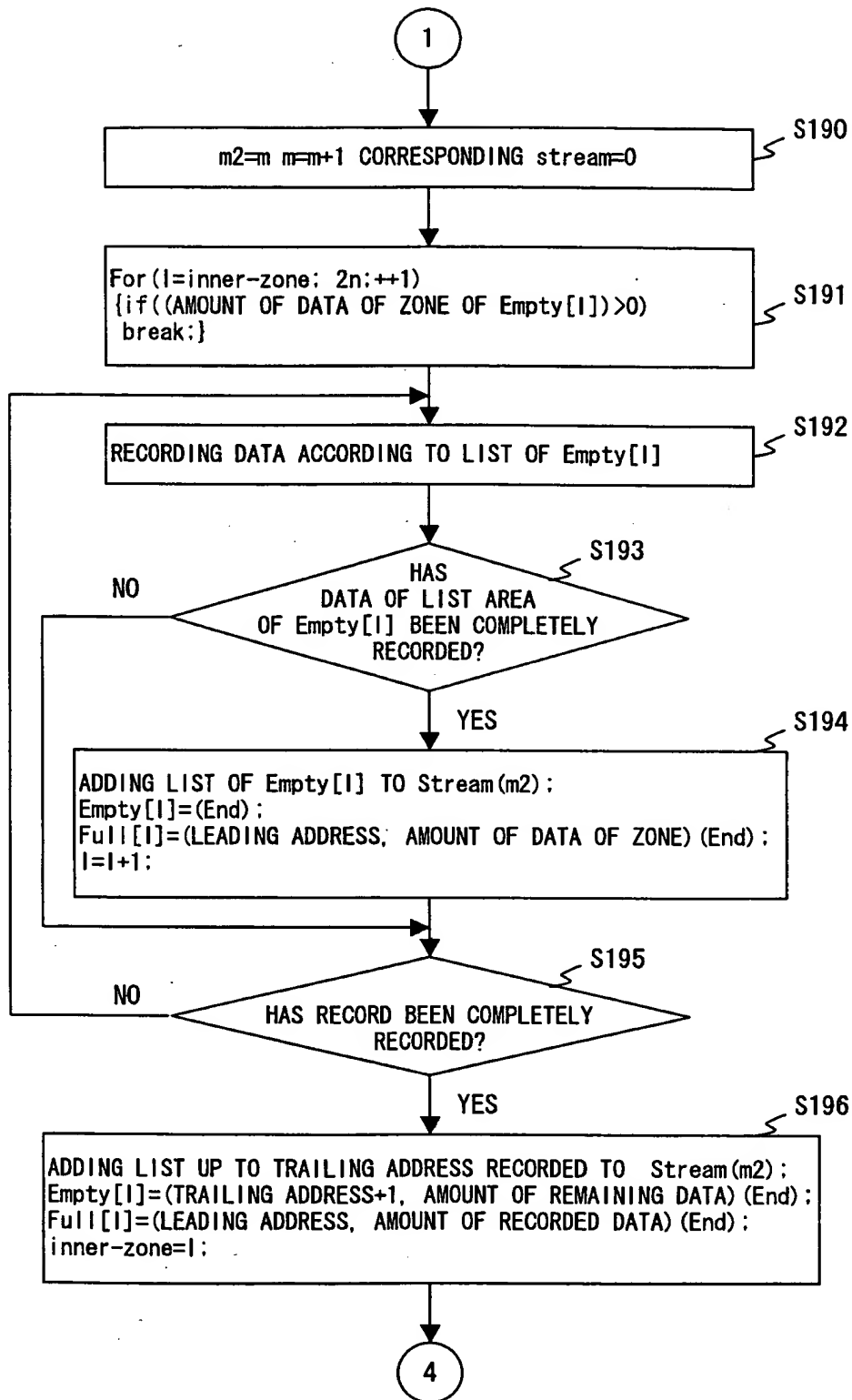


FIG. 59

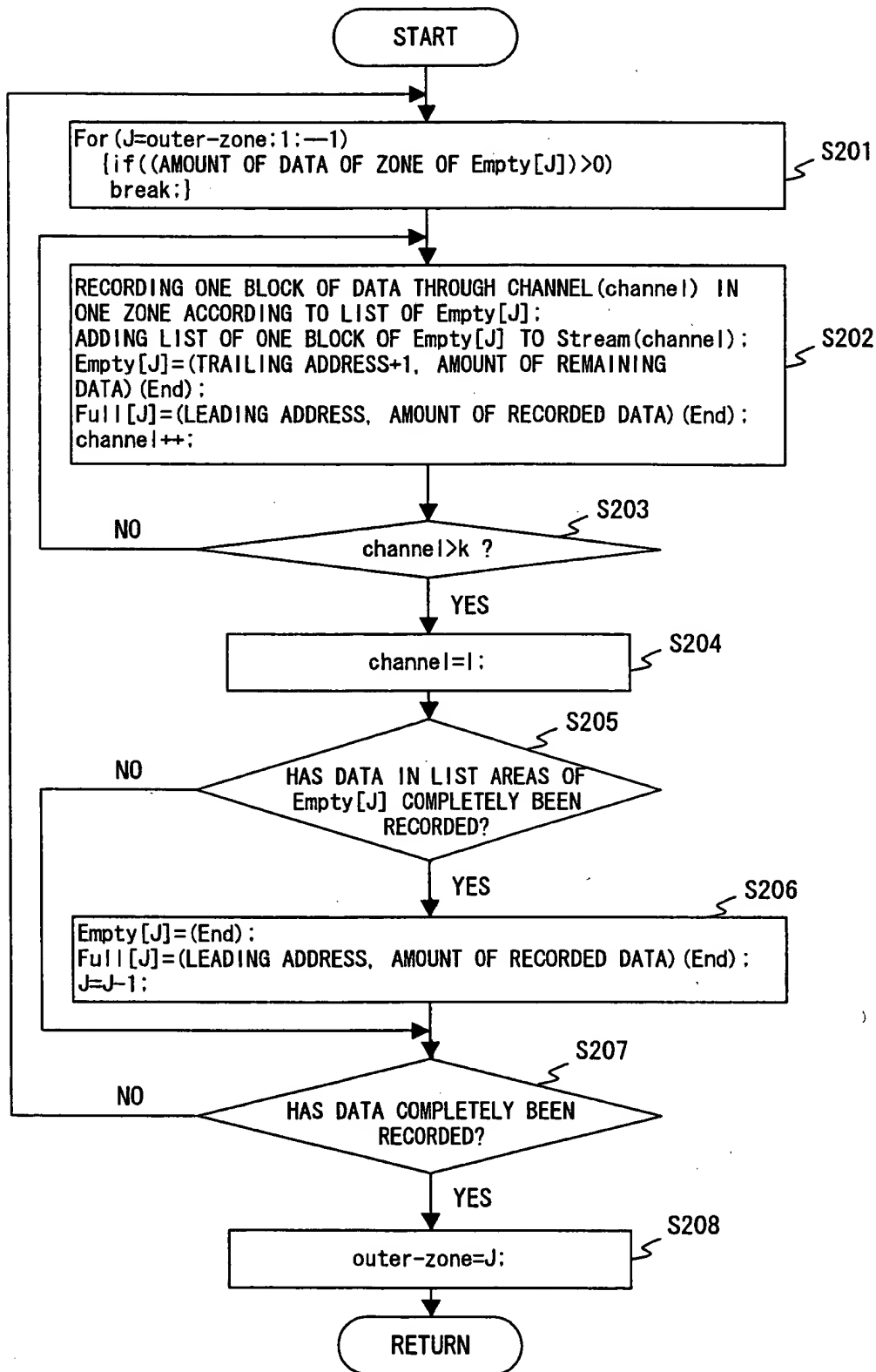


FIG. 60